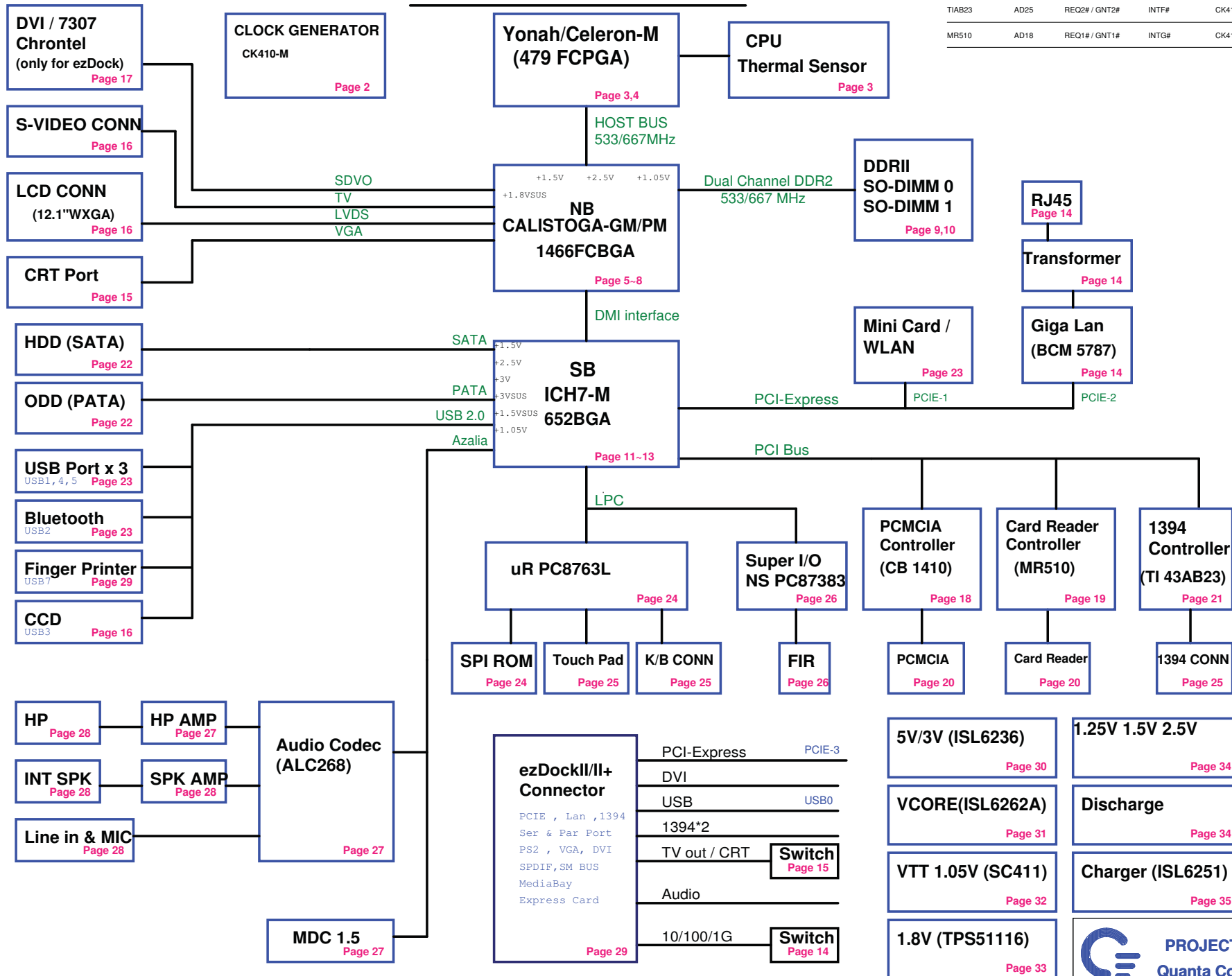
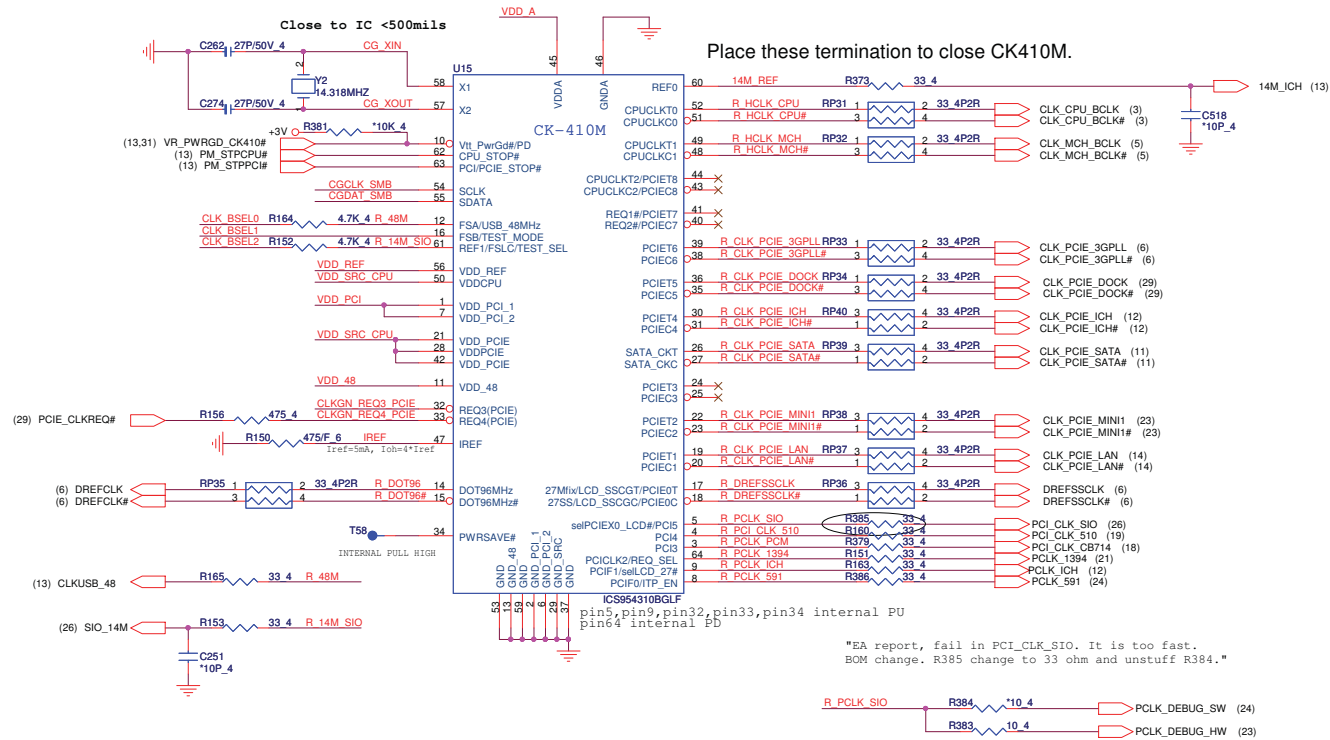
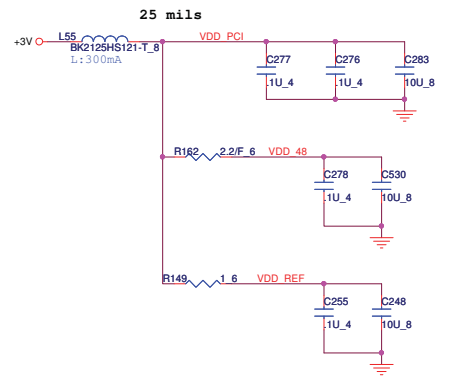


# ZU2 SYSTEM BLOCK DIAGRAM

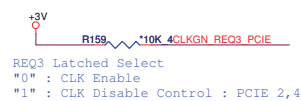
PCI DEVICE	IDSEL#	REQ# / GNT#	Interrupts	CLOCK
CB1410	AD17	REQ0# / GNT0#	INTE#	CK410/PC13
TIAB23	AD25	REQ2# / GNT2#	INTF#	CK410/PC18
MR510	AD18	REQ1# / GNT1#	INTG#	CK410/PC14



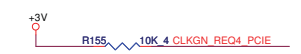




### PCIE CLK enable/disable control

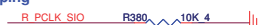


```
REQ3 Latched Select
"0" : CLK Enable
"1" : CLK Disable Control : PCIE 2,4
```



```
REQ2 Latched Select
"0" : CLK Enable
"1" : CLK Disable Control : PCIE 3,5,7
```

## Starpping



Latched Select. (Pin 17,18)  
 "0" : LCD CLK  
 "1" : PCIEX CLK



```
ITP/SRC8 SELECT
0: SRC8
1: ITP
```



SELLCD 27# Select. (Pin 17,18)  
 "0" : 27MHzSS/27MHzSS# pair  
 "1" : LCD CLK pair

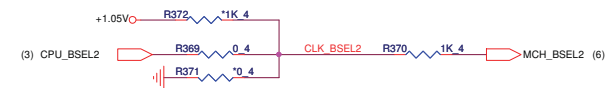
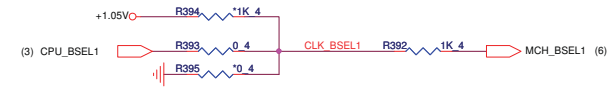


PCIE CLK/REQ select  
"0" : PCIE CLK  
"1" : REQ pin

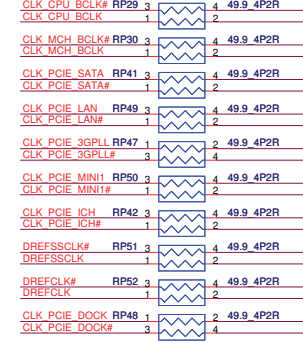
### Frequency select

SEL2		SEL1	SEL0		
FSC	F5B	F5A	CPU	SRC	PCI
1	0	1	100	100	33
0	0	1	133	100	33
0	1	1	166	100	33
0	1	0	200	100	33
0	0	0	266	100	33
1	0	0	333	100	33
1	1	0	400	100	33
1	1	1	200	100	33

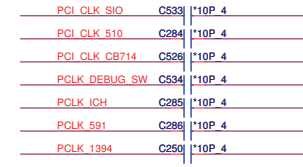
BSEL strappings need to be set for 533MHz Moby Dick  
(Intel?915GM - Calistoga Interposer)  
(if Calistoga is designed for 667MHz board).



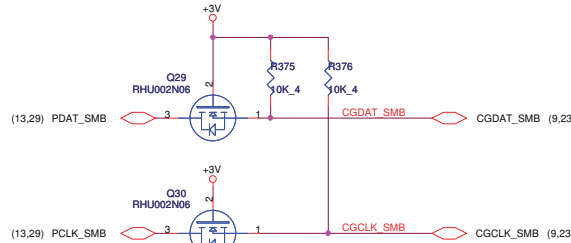
### Terminal Resistor



## Reserve for EMI

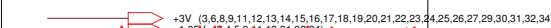


## SM BUS level shift



CLK SMB/PDAT SMB(SB&LAN&MINI CARD&EZ,+3V S5) CGCLK SMB/CGDAT SMB(CLK GEN&DDR,+3V

### Power check



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PROJECT : ZU2  
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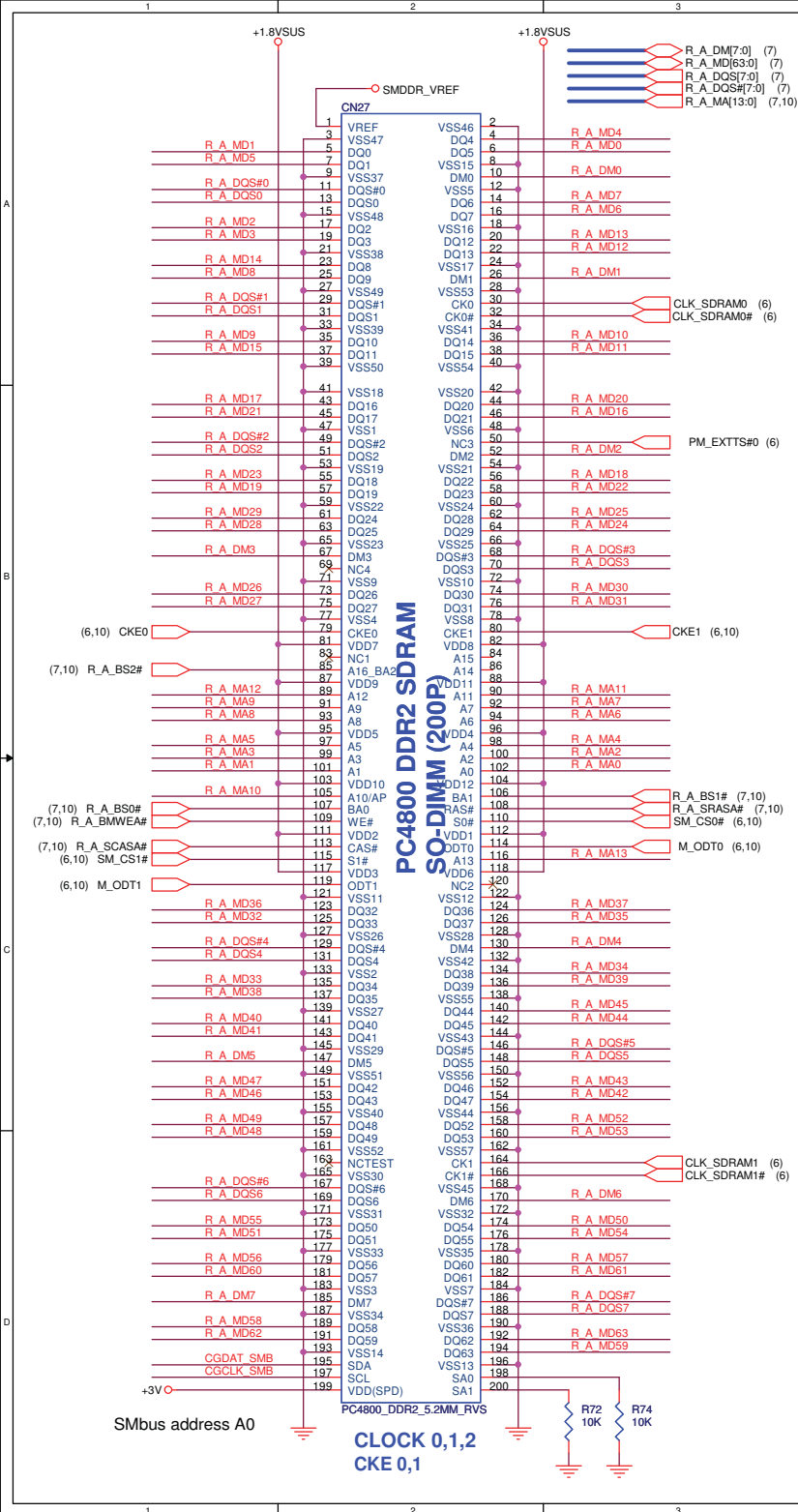






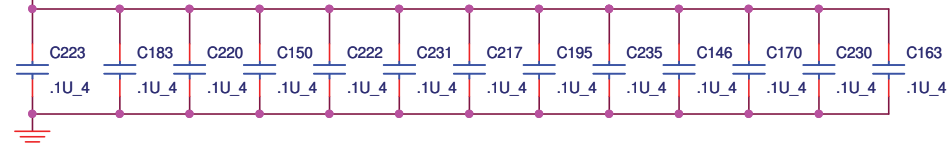






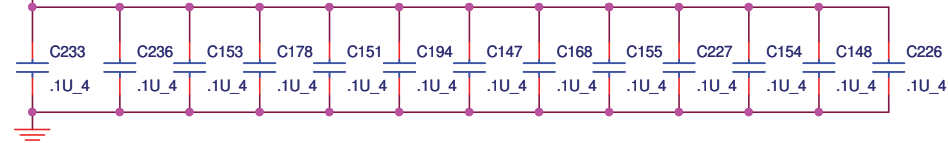


SMDDR\_VTERM



Layout note: Place one cap close to every 2 pull resistors terminated to SMDDR\_VTERM

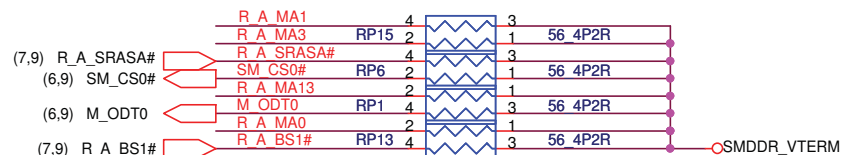
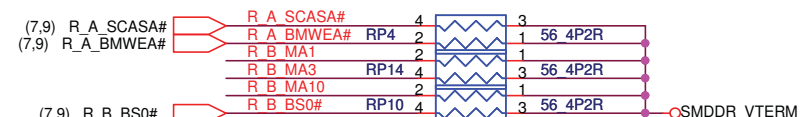
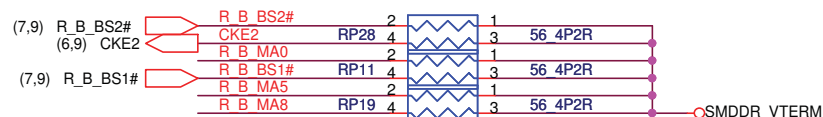
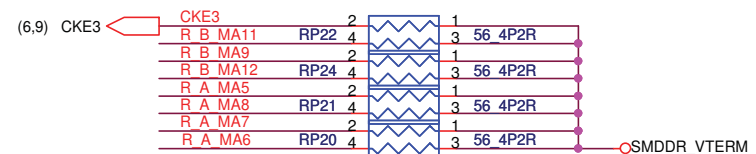
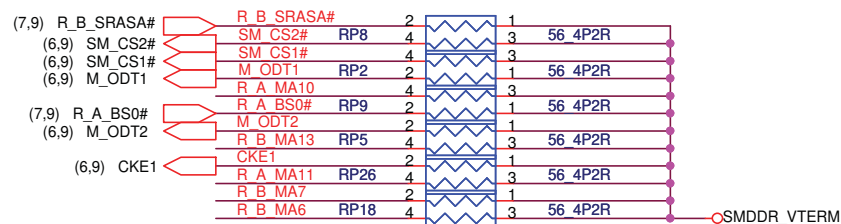
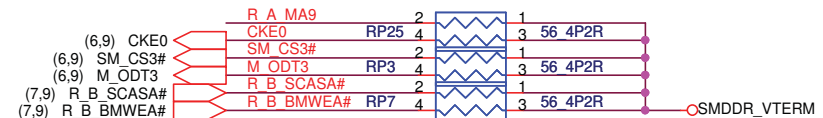
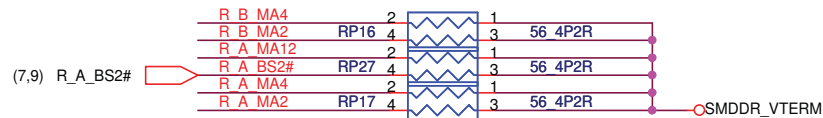
SMDDR\_VTERM




Layout note: Place one cap close to every 2 pull resistors terminated to SMDDR\_VTERM

R\_A\_MA[0..13] (7,9)

R\_B\_MA[0..13] (7,9)

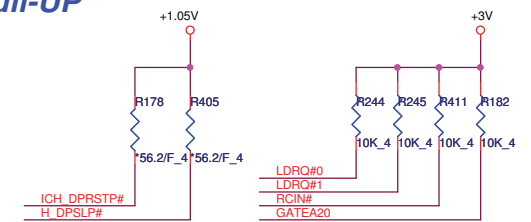
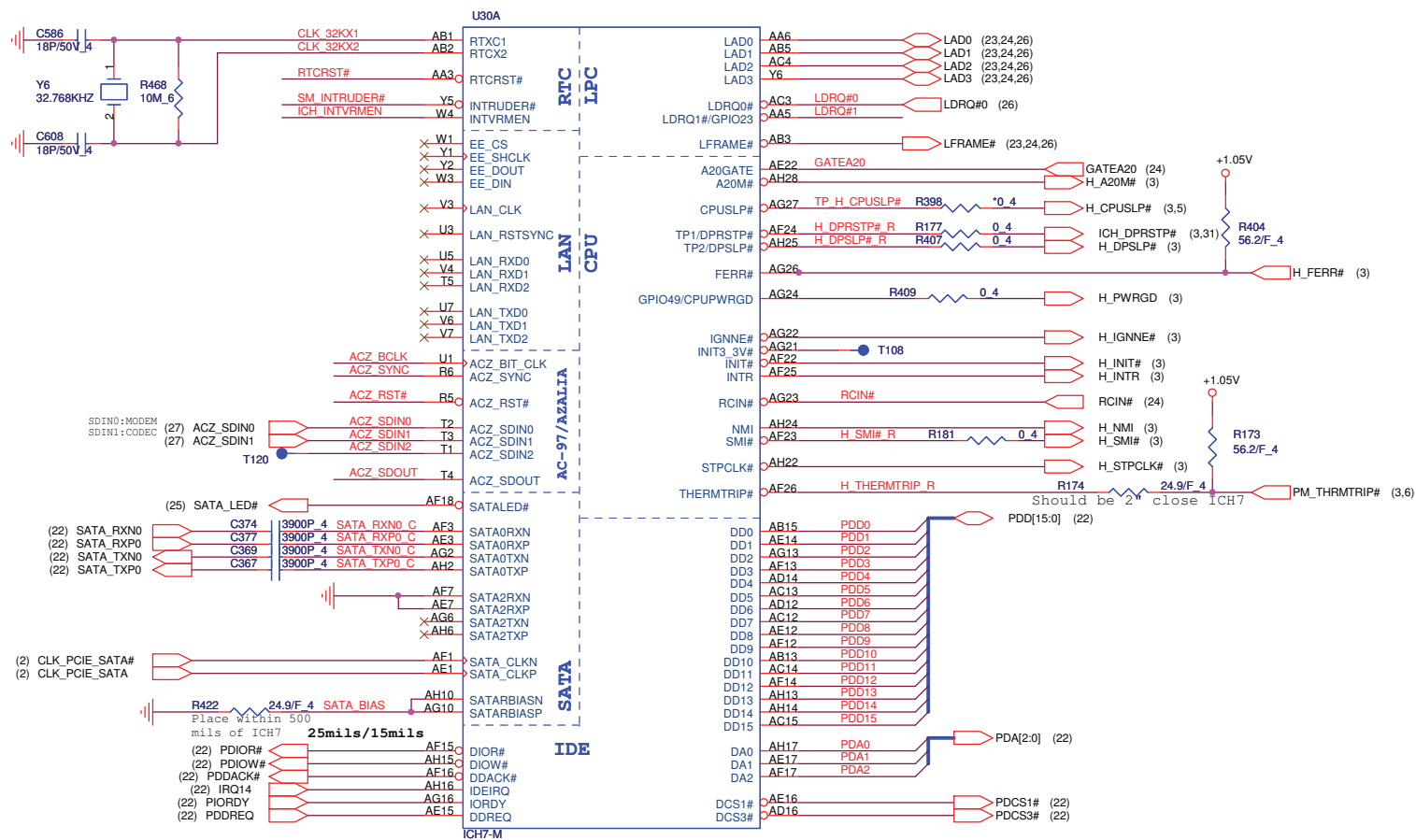




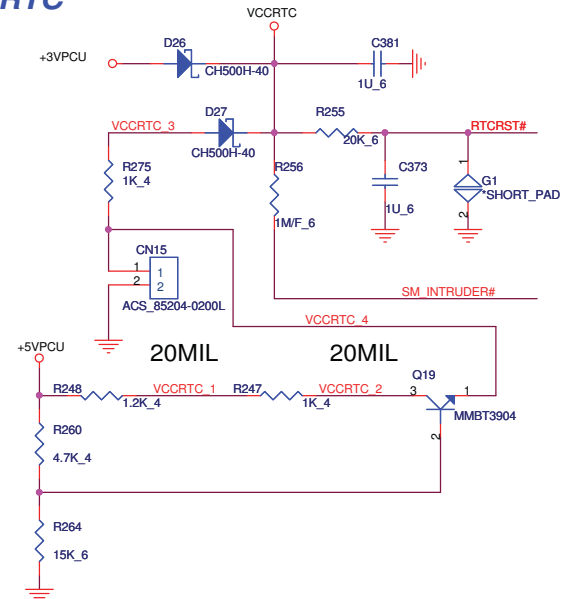
**PROJECT : ZU2**  
**Quanta Computer Inc.**

Size	Document Number <b>DDR2 RES. ARRAY</b>	Rev <b>1A</b>
Date:	Friday, March 23, 2007	Sheet 10 of 39



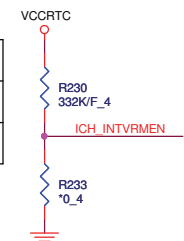


**RTC**



```
ICH7 internal VR
enable strap
```

	INTVRME
Enable (default)	1
Disable	0



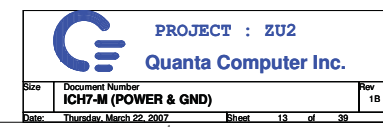
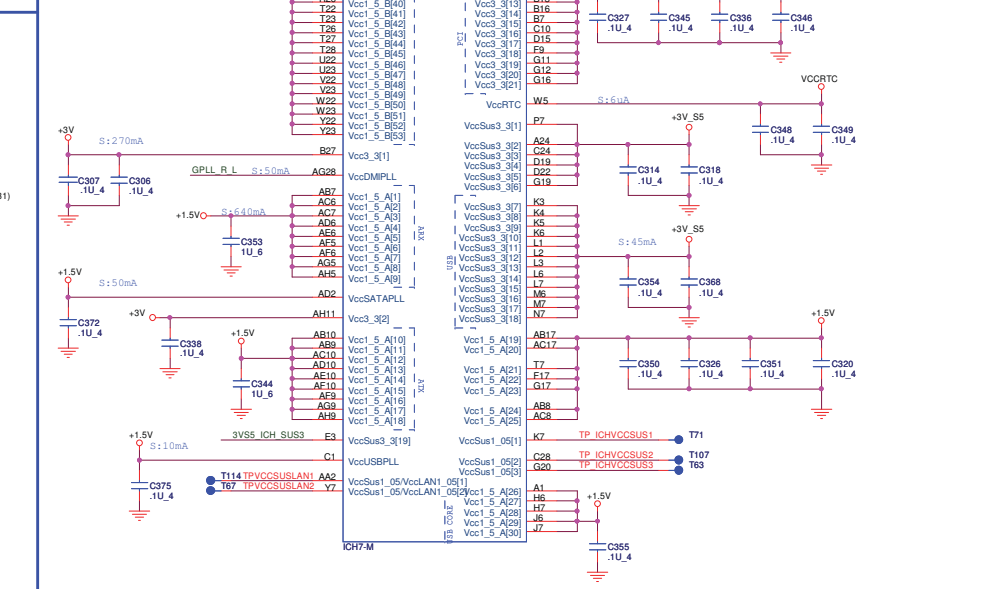
**PROJECT : ZU2**  
**Quanta Computer Inc.**

Size	Document Number <b>ICH7-M (CPU, SATA, IDE,LPC)</b>	Rev <b>1A</b>
Date:	Thursday, March 22, 2007	Sheet 11 of 39

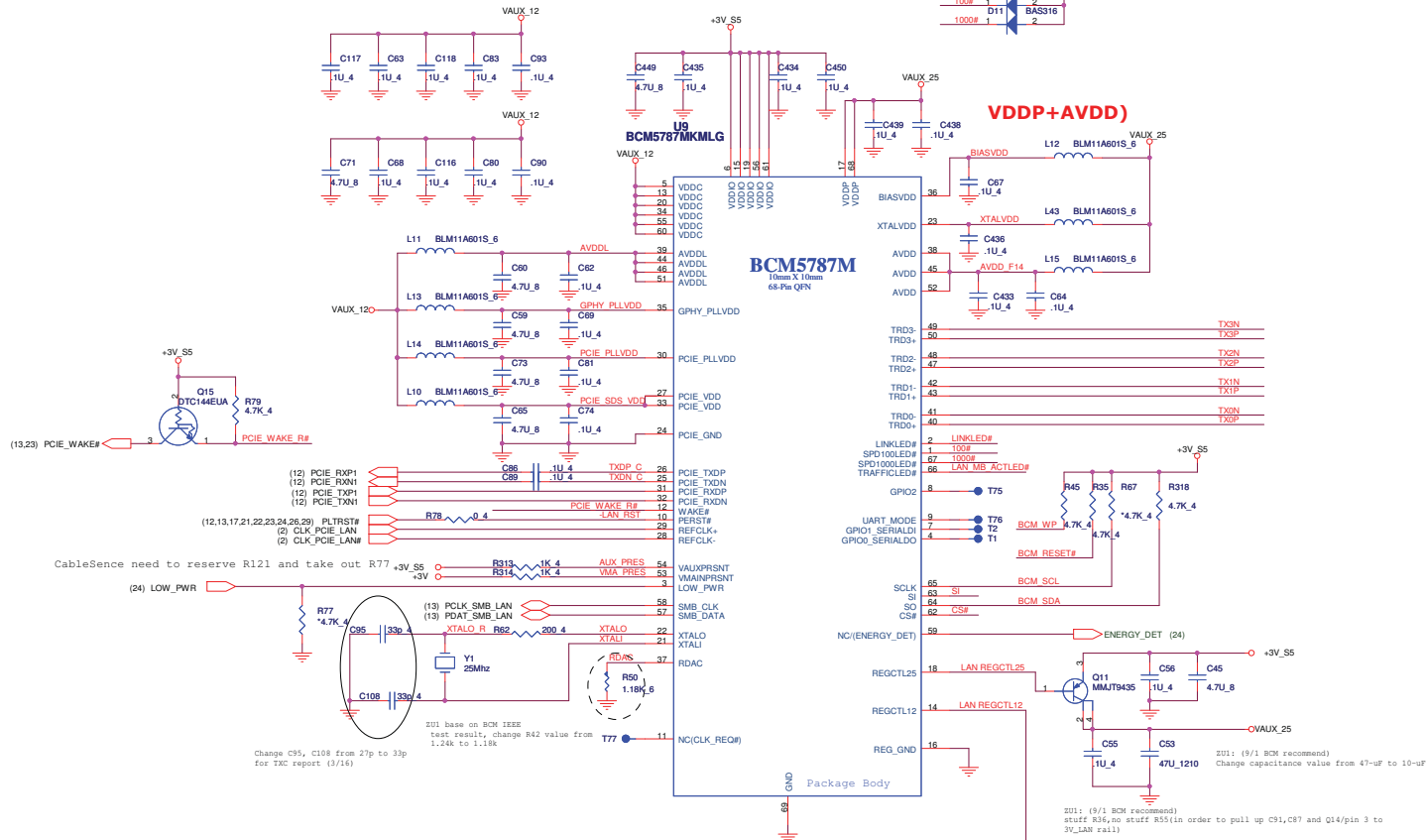




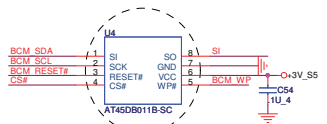




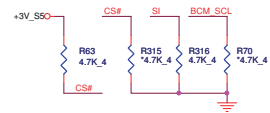




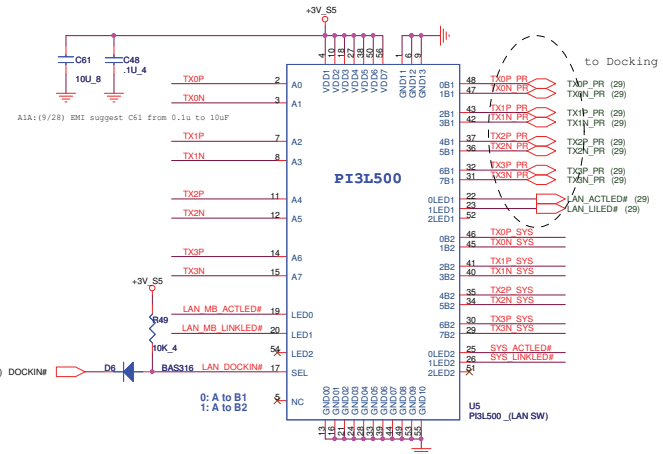
## Flash ROM/EEPROM



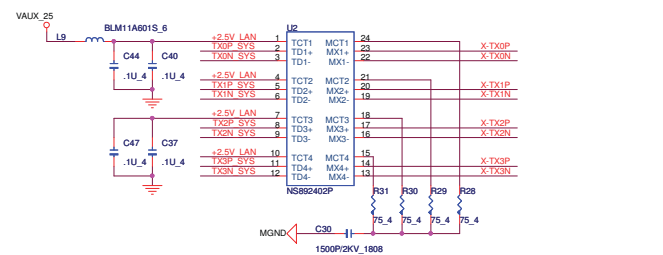
	SO	SI	CS#	SCLK
24c64	1	1	0	1
AT45DB011B	1	0	1	1



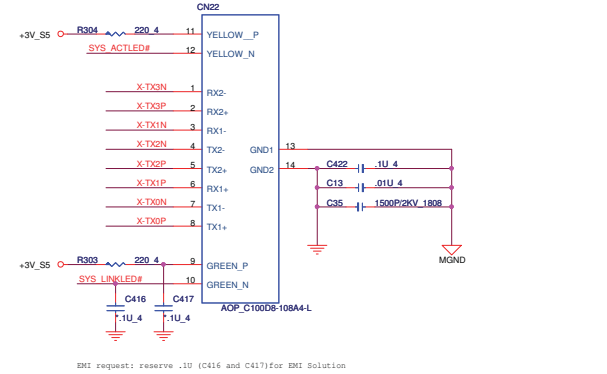
## LAN Switch



## LAN Transformer

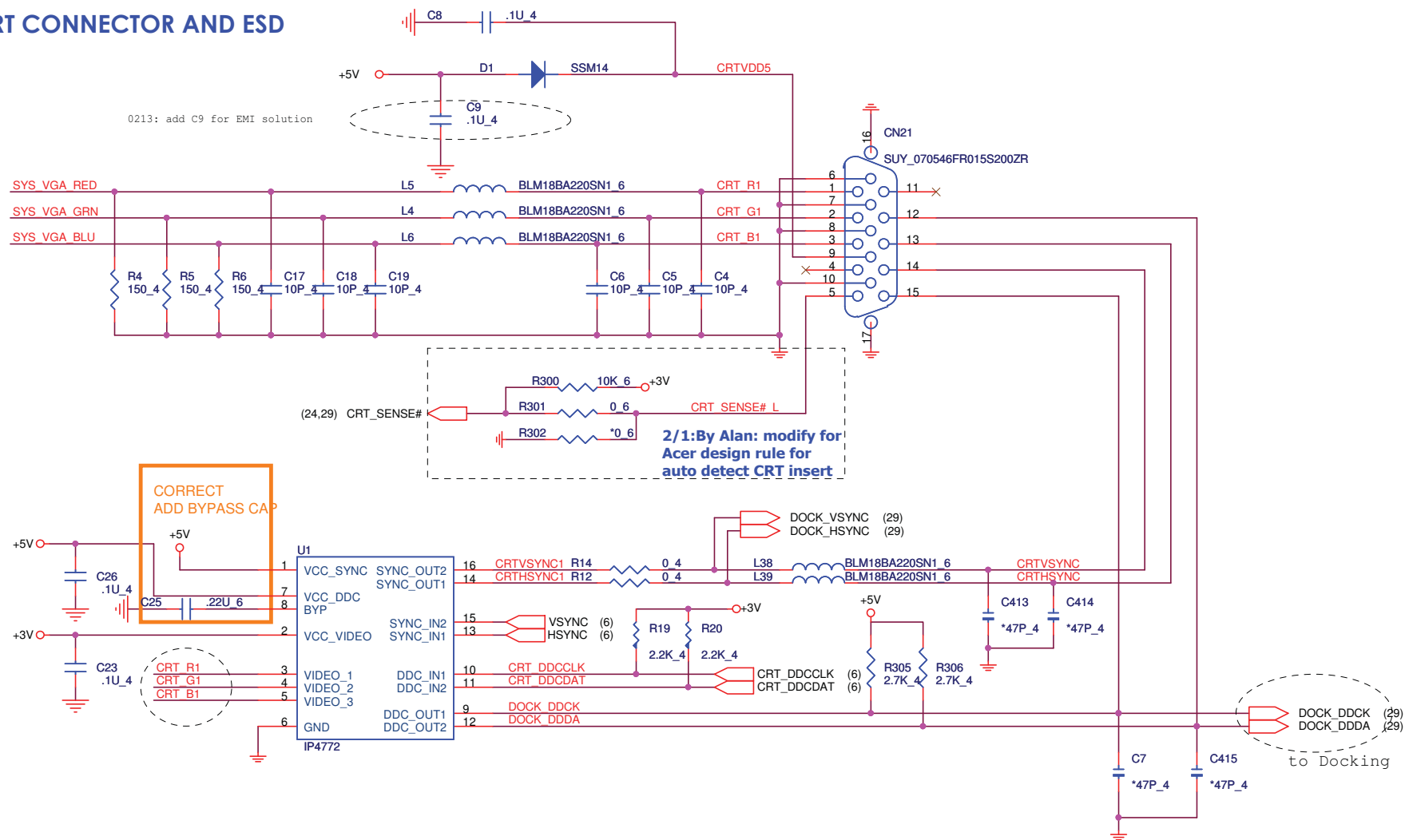


## RJ45

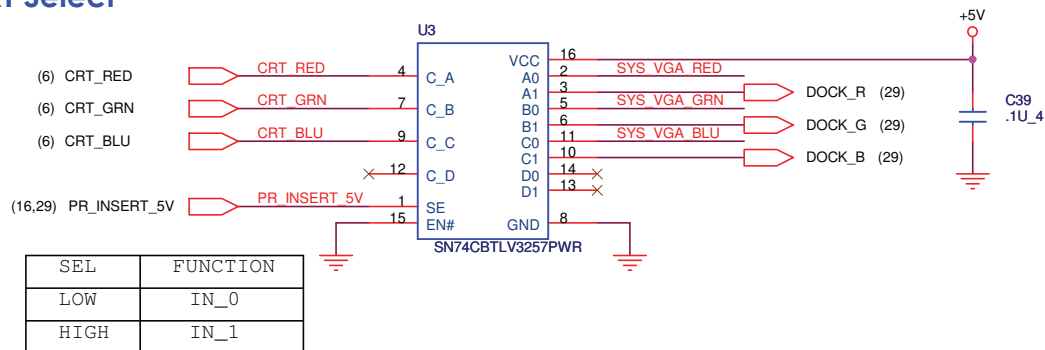




## CRT CONNECTOR AND ESD



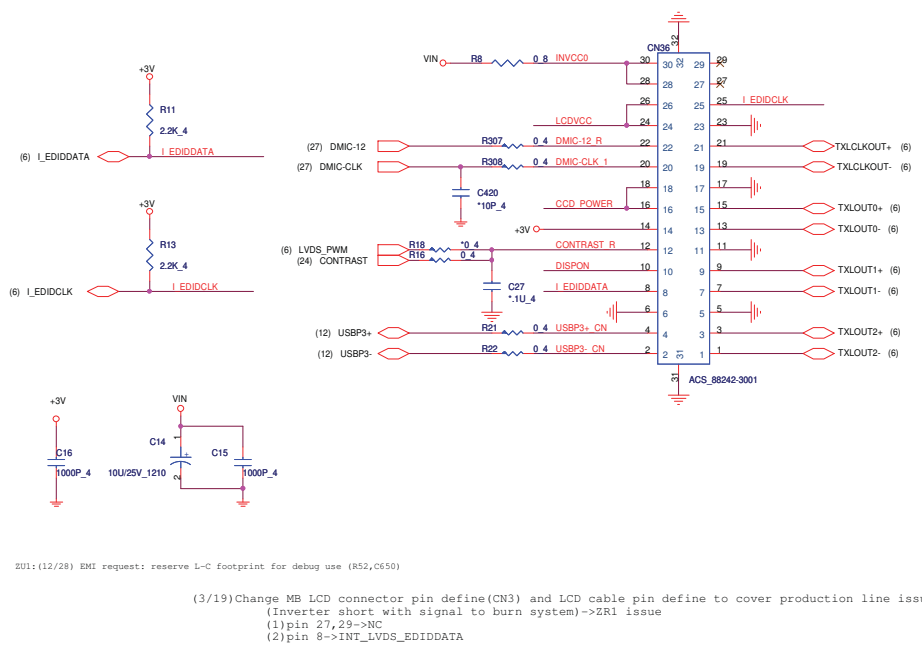
## CRT Select



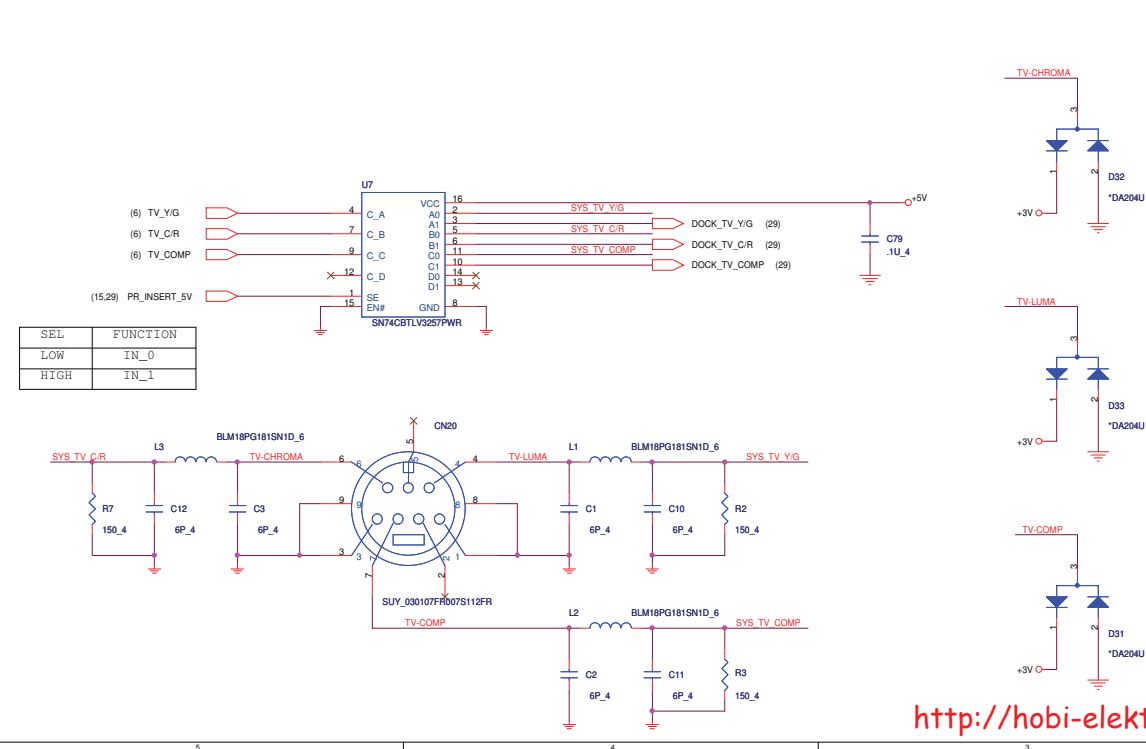
PROJECT : ZU2  
Quanta Computer Inc.



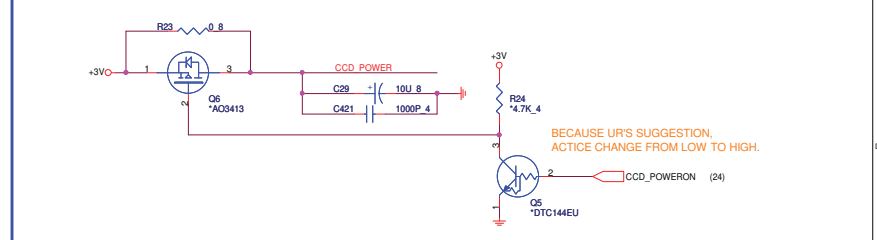
LVDS



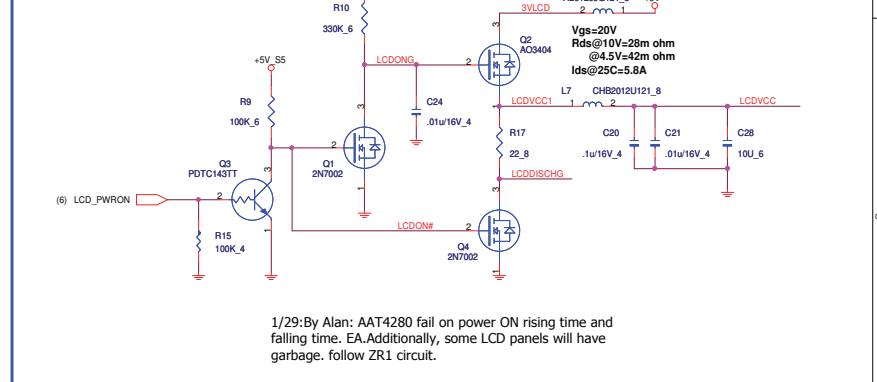
TV Out (SVHS) MiniDIN 7-pin



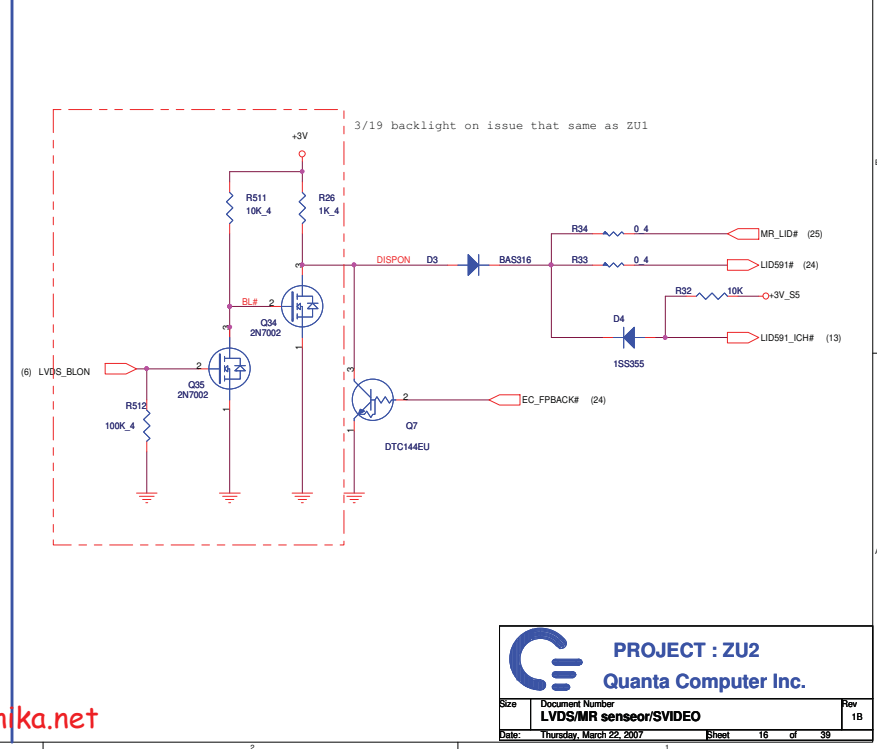
CAMERA MODULE



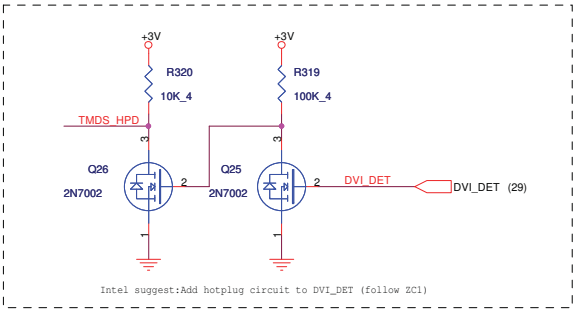
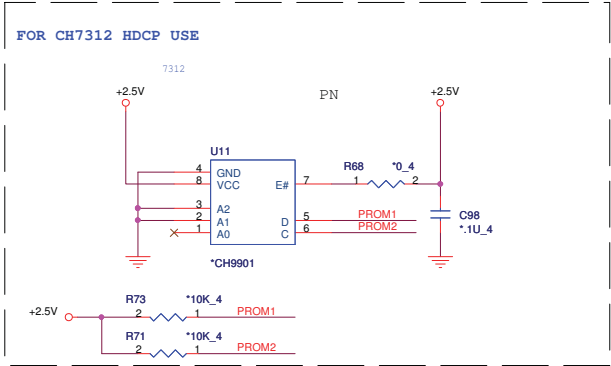
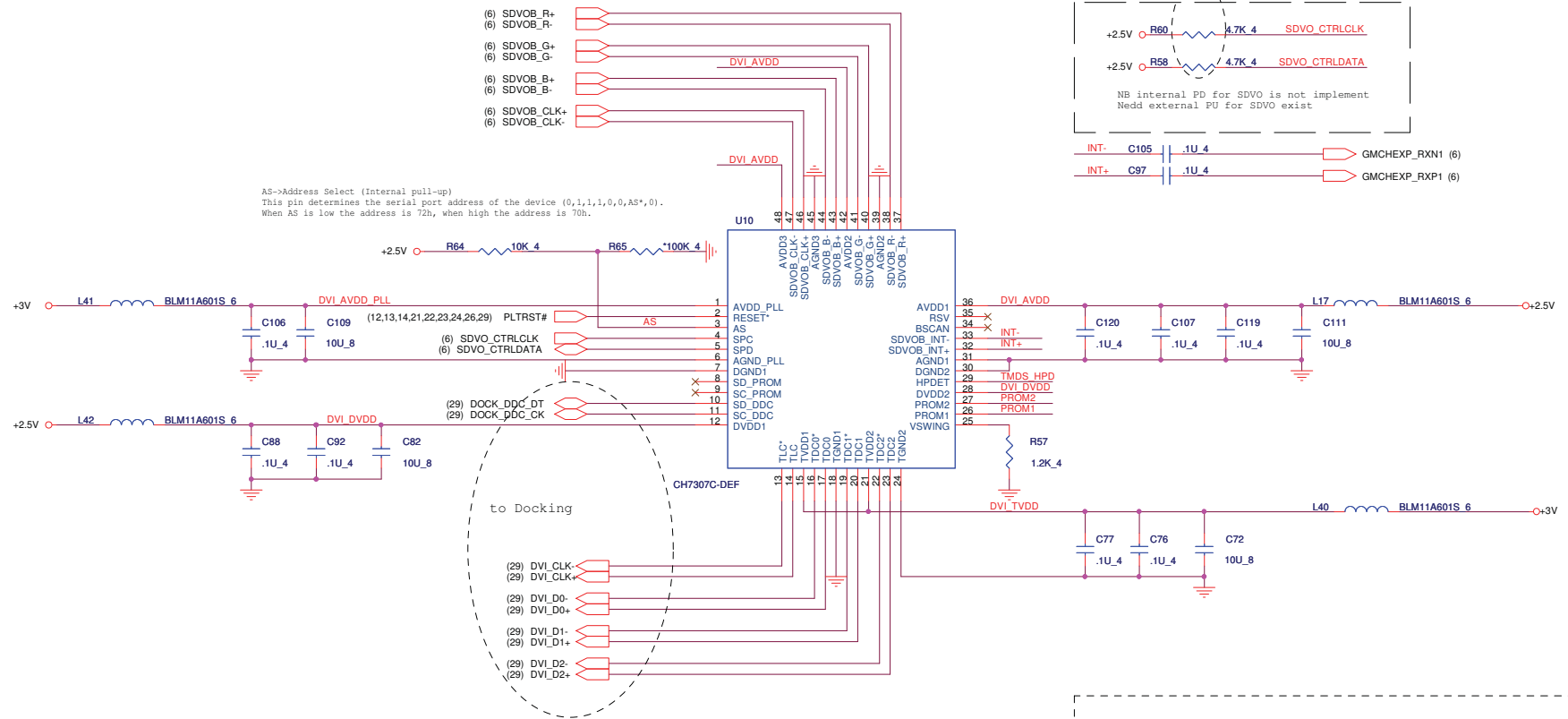
LCD Power



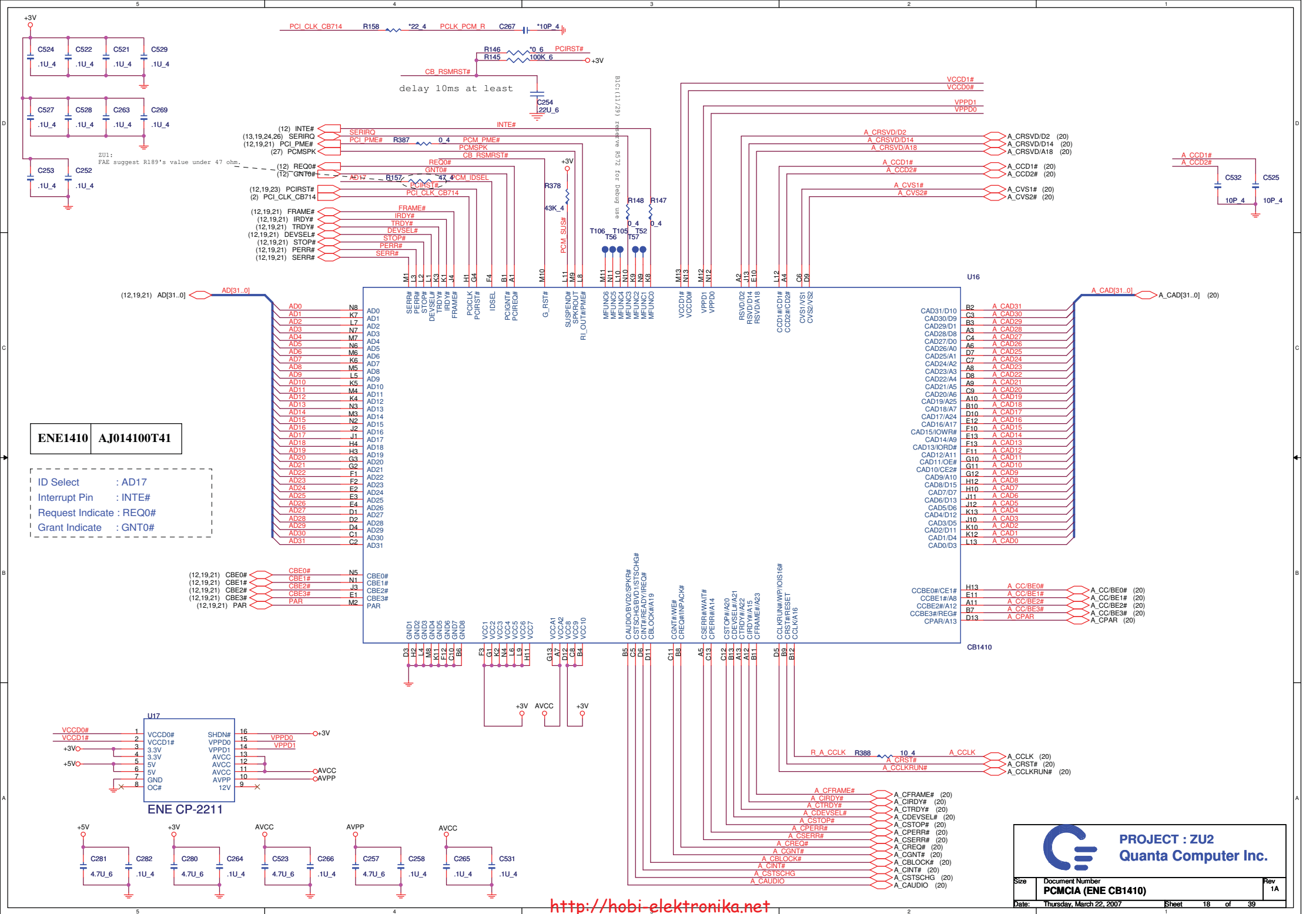
MR Sensor





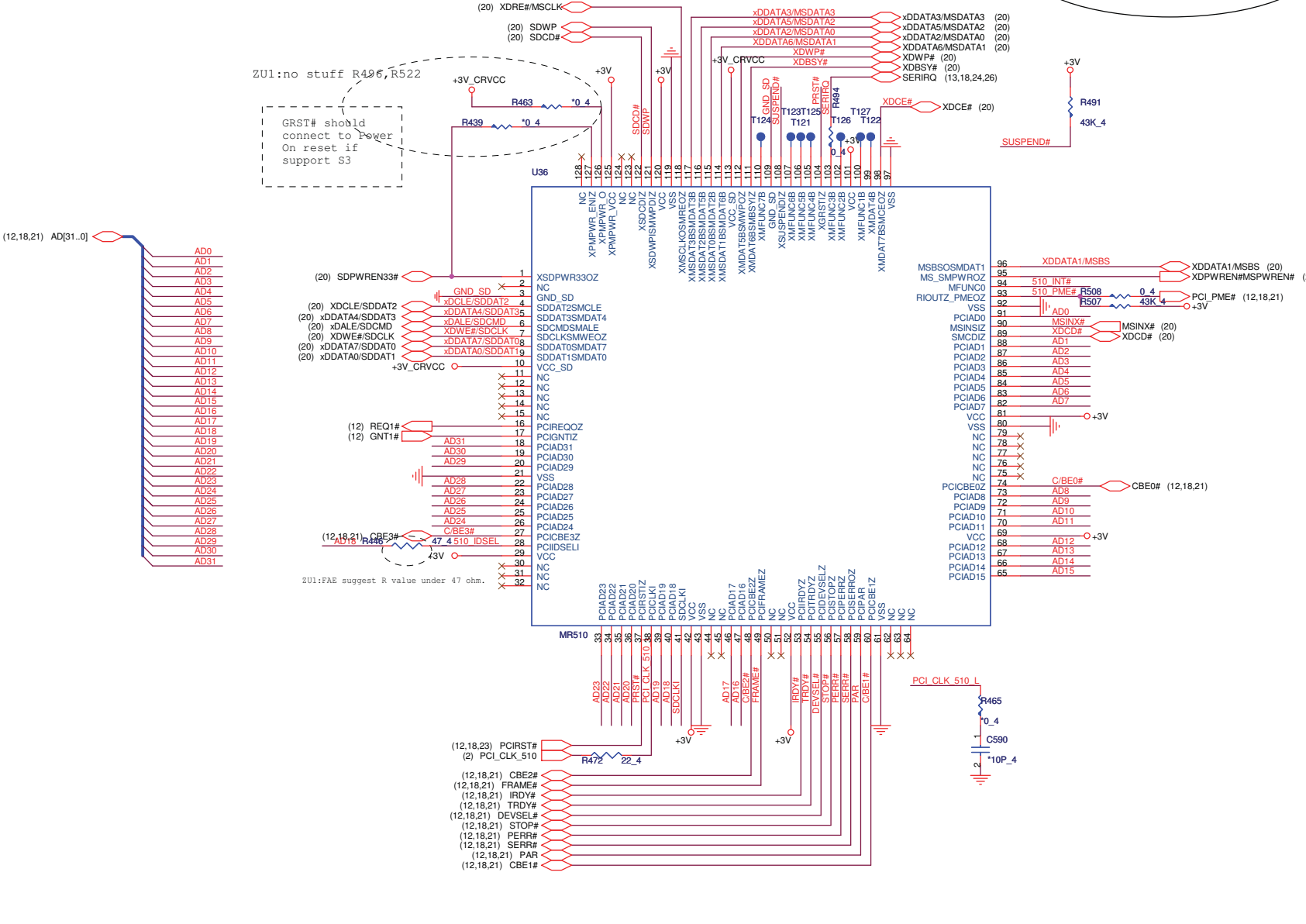




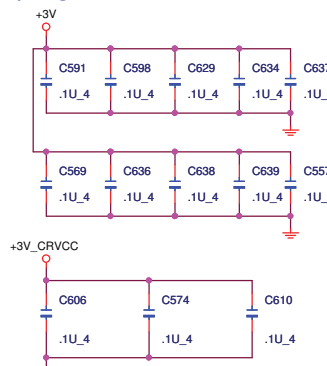




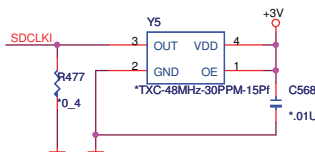
```
ID Select      : AD18
Interrupt Pin   : INTG#
Request Indicate : REQ1#
Grant Indicate  : GNT1#
```



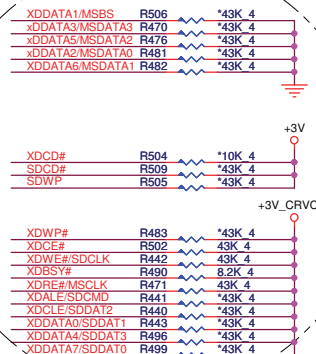
## Decoupling CAP.



## 48MHz CLK



PU/PD



0213: follow ZU1 for cost down issue with FAE

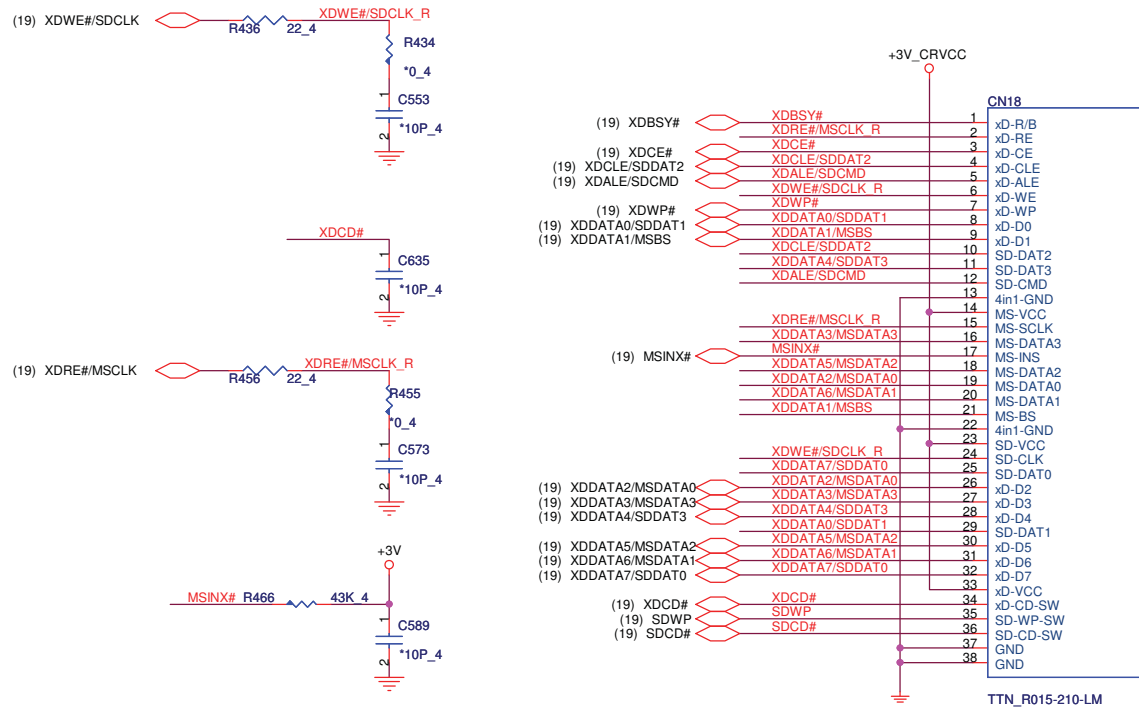


**PROJECT : ZU2**  
**Quanta Computer Inc.**

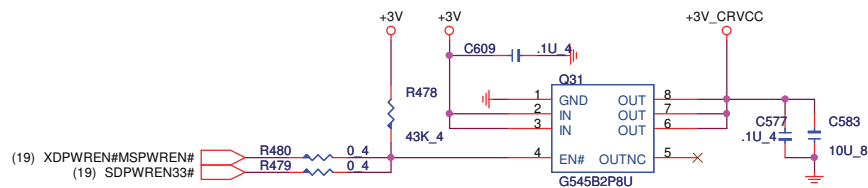
Size	Document Number	Rev
	<b>Card Reader (MR510)</b>	1
Date	Wednesday, March 28, 2007	Sheet 19 of 39



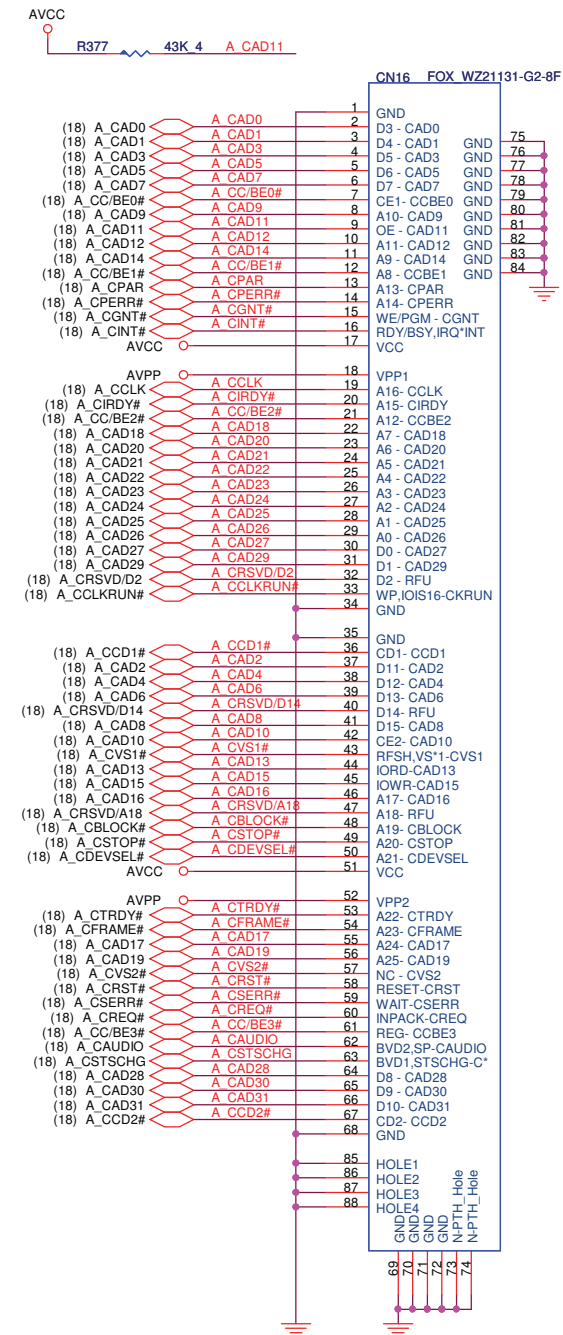
## CardReader



## CardReader Power switch



## PCMCIA



**PROJECT : ZU2**  
**Quanta Computer Inc.**

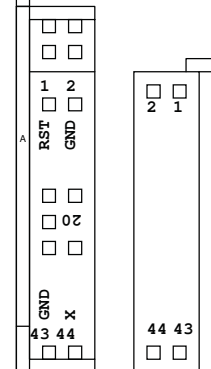
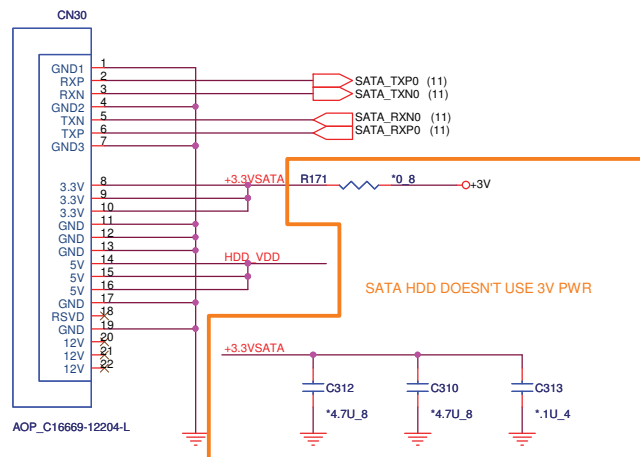
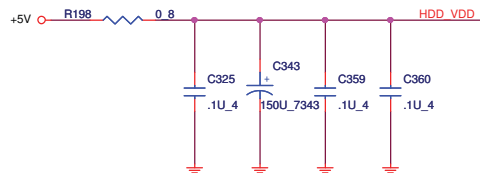
Size	Document Number	Rev
	<b>CARD Reader &amp; PCMCIA SLOT</b>	1A
Date:	Thursday, March 22, 2007	Sheet 20 of 39



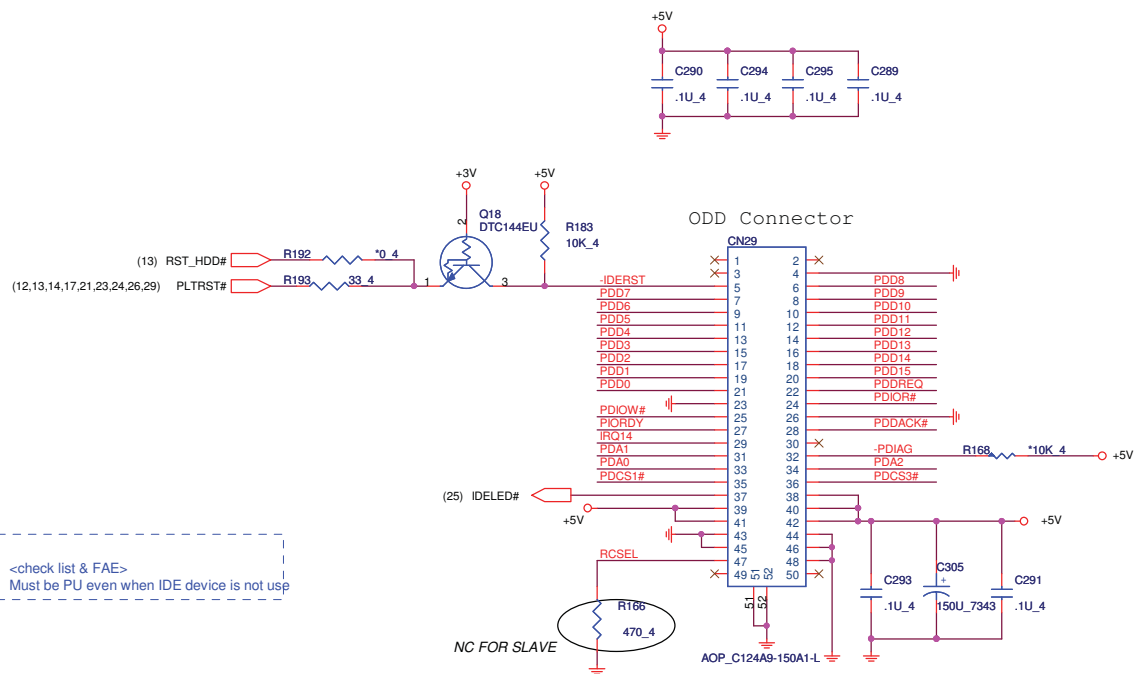
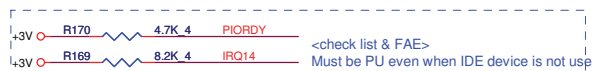
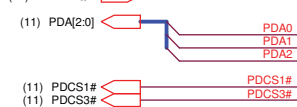
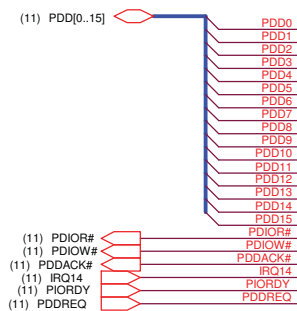




## SATA HDD

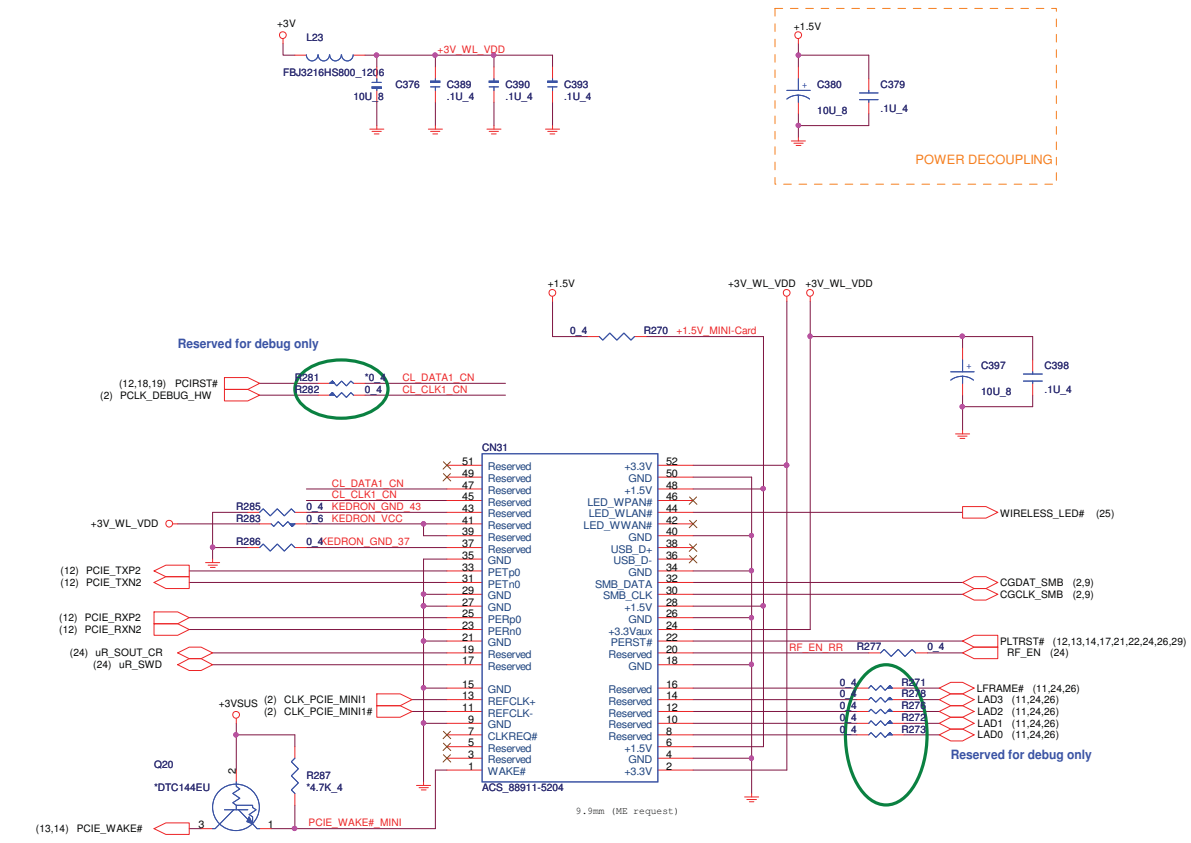


## PATA ODD

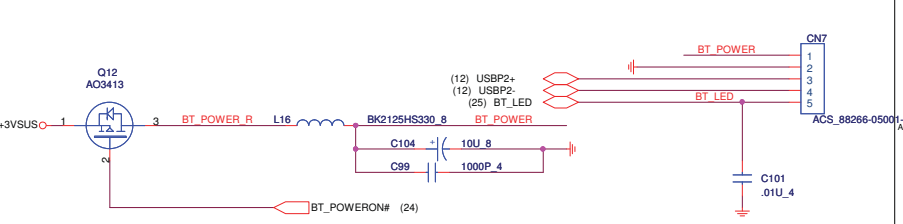




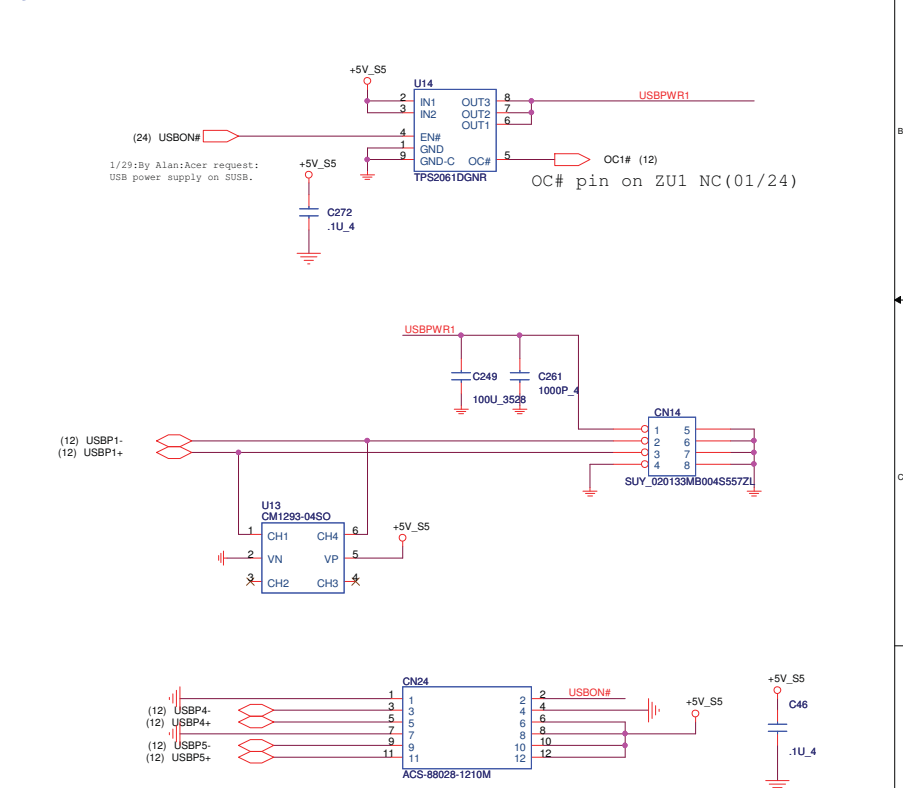
MINI-Card



BLUETOOTH MODULE CONNECTOR



System USB

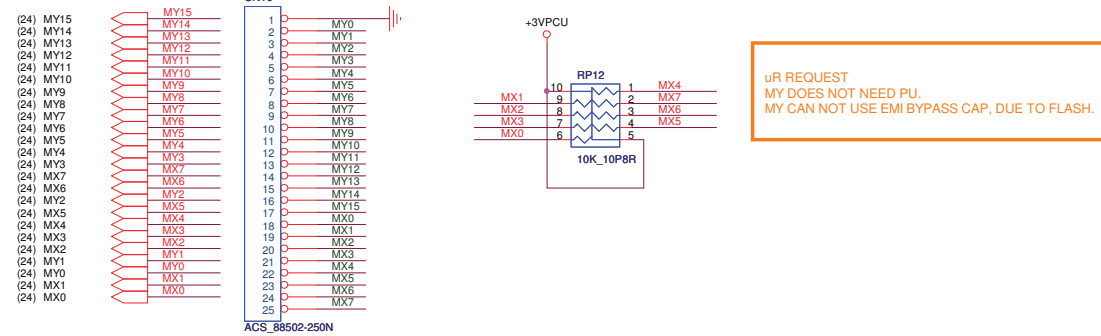




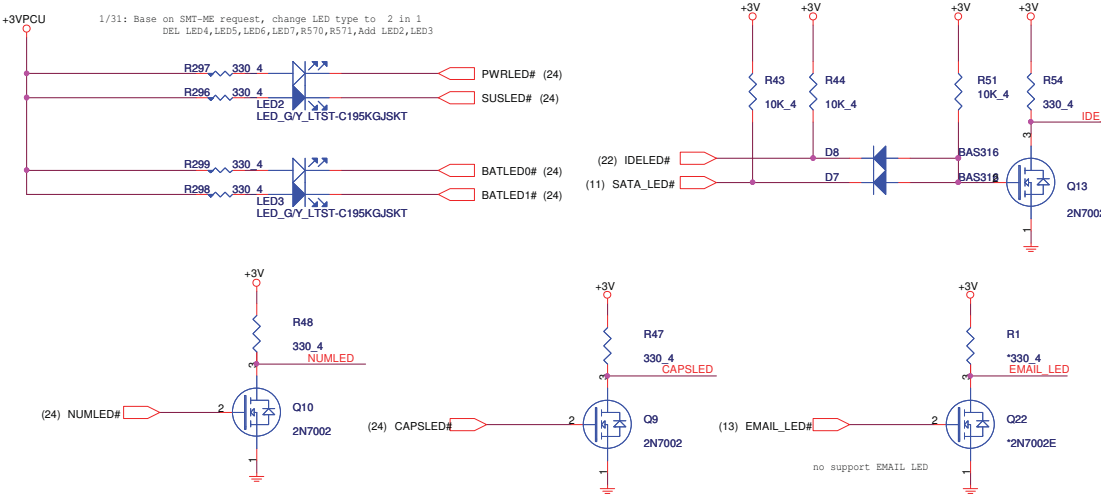




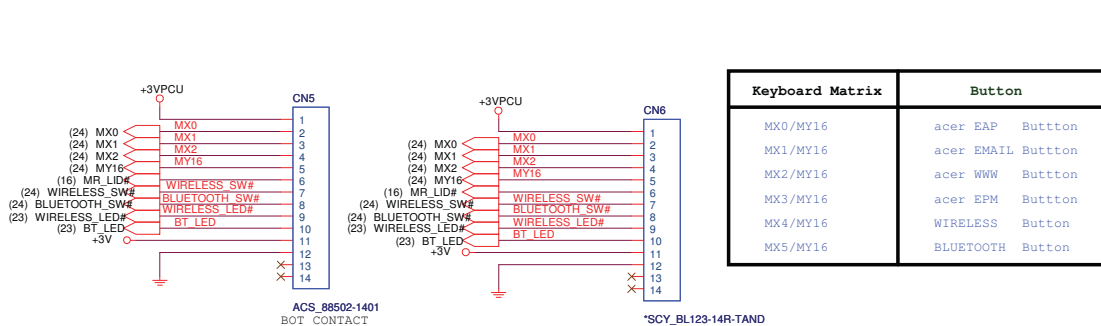
INT K/B



LED

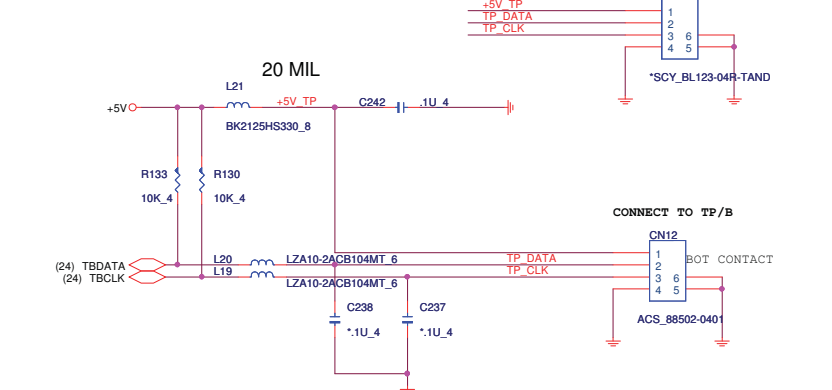


Function Board

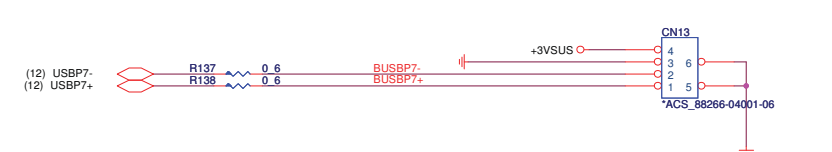


Keyboard Matrix	Button
MX0/MY16	acer EAP Button
MX1/MY16	acer EMAIL Button
MX2/MY16	acer WW Button
MX3/MY16	acer EPM Button
MX4/MY16	WIRELESS Button
MX5/MY16	BLUETOOTH Button

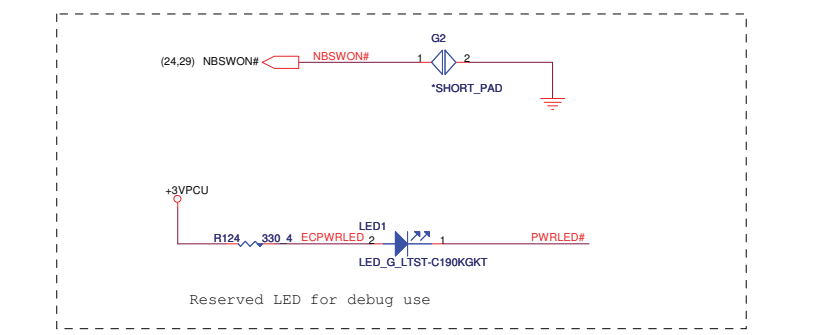
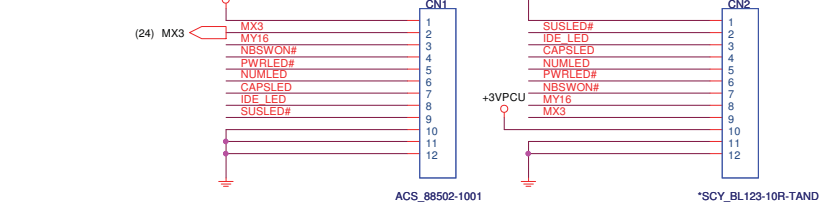
TOUCH PAD



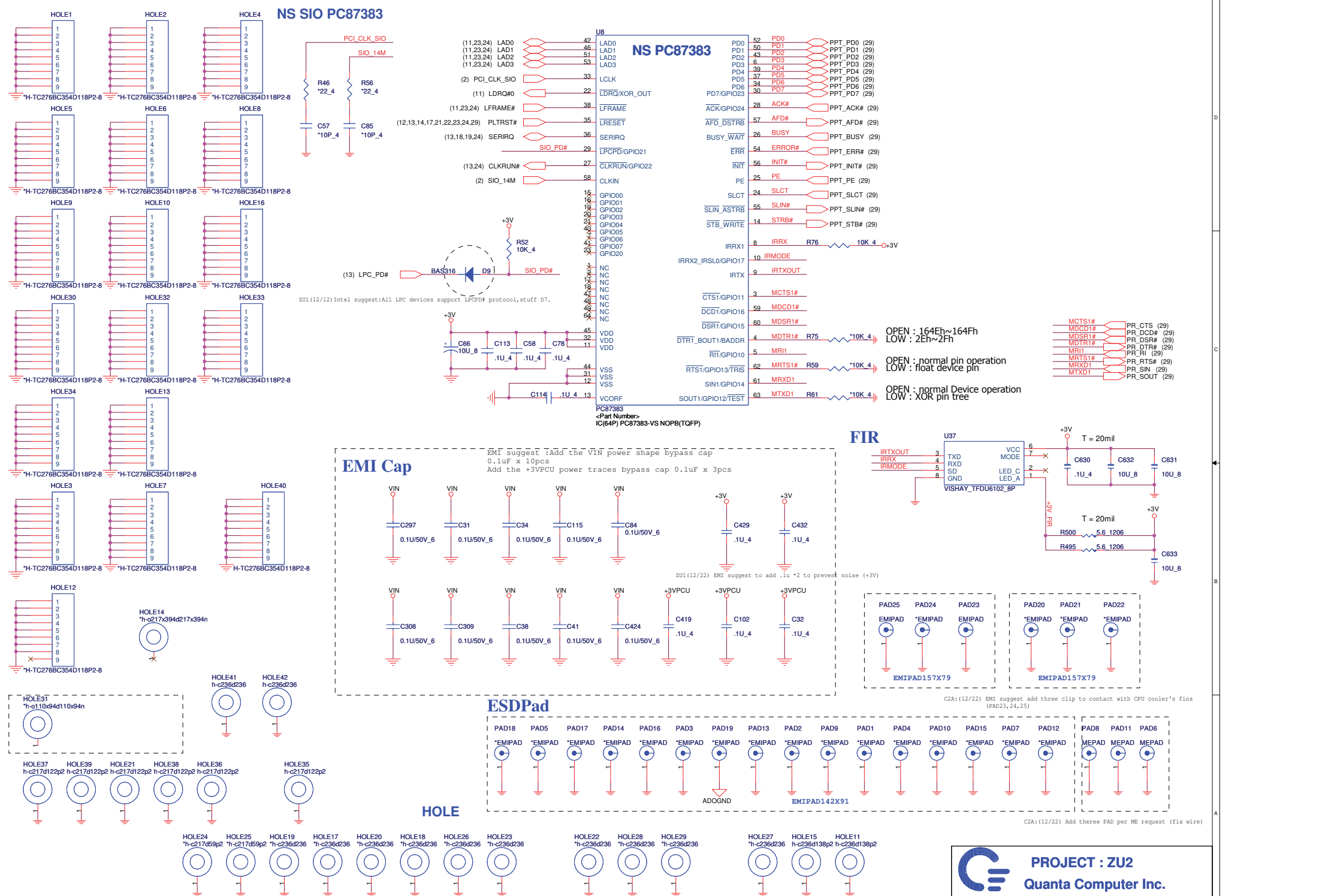
Finger Printer



LED Board

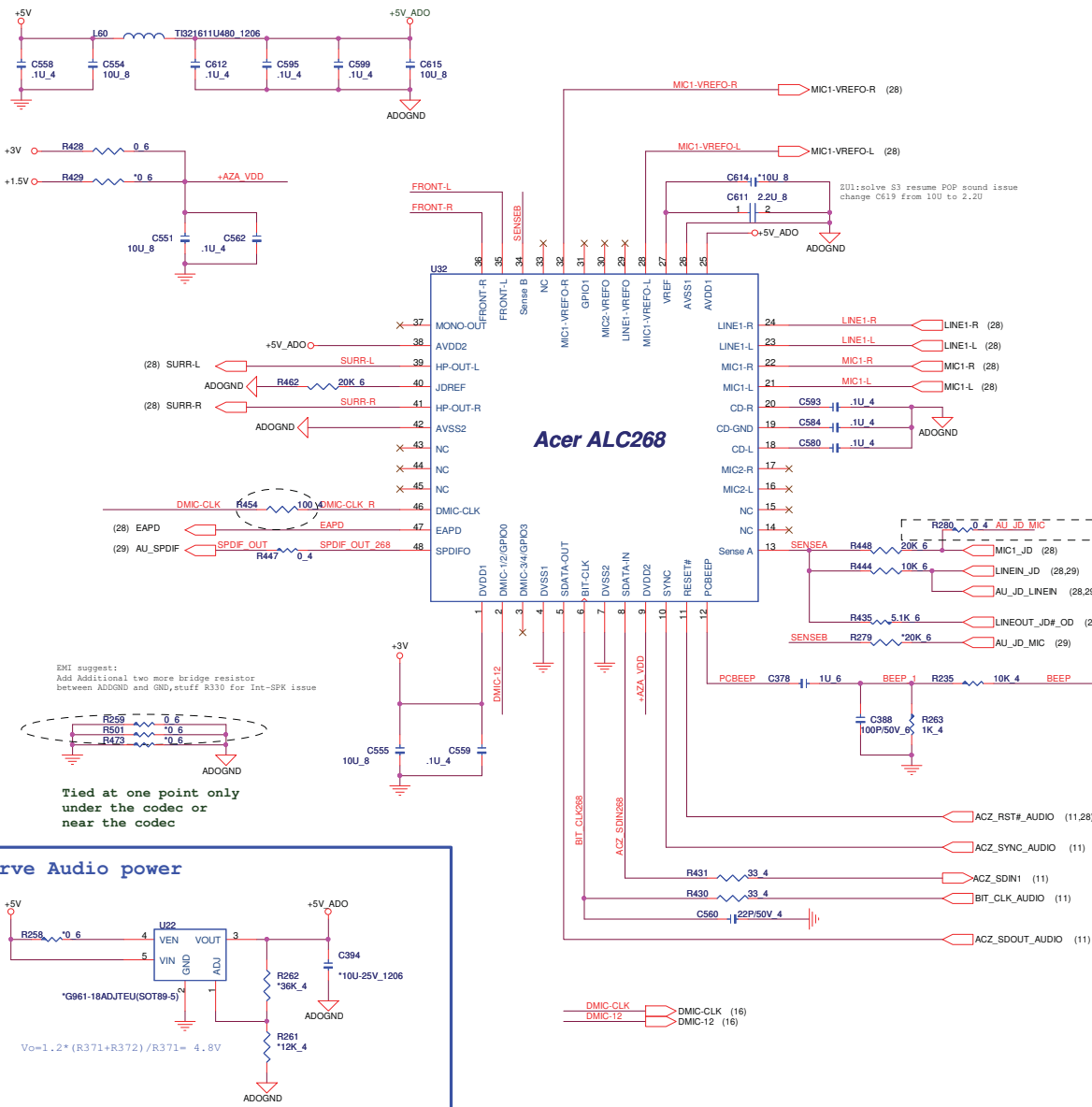




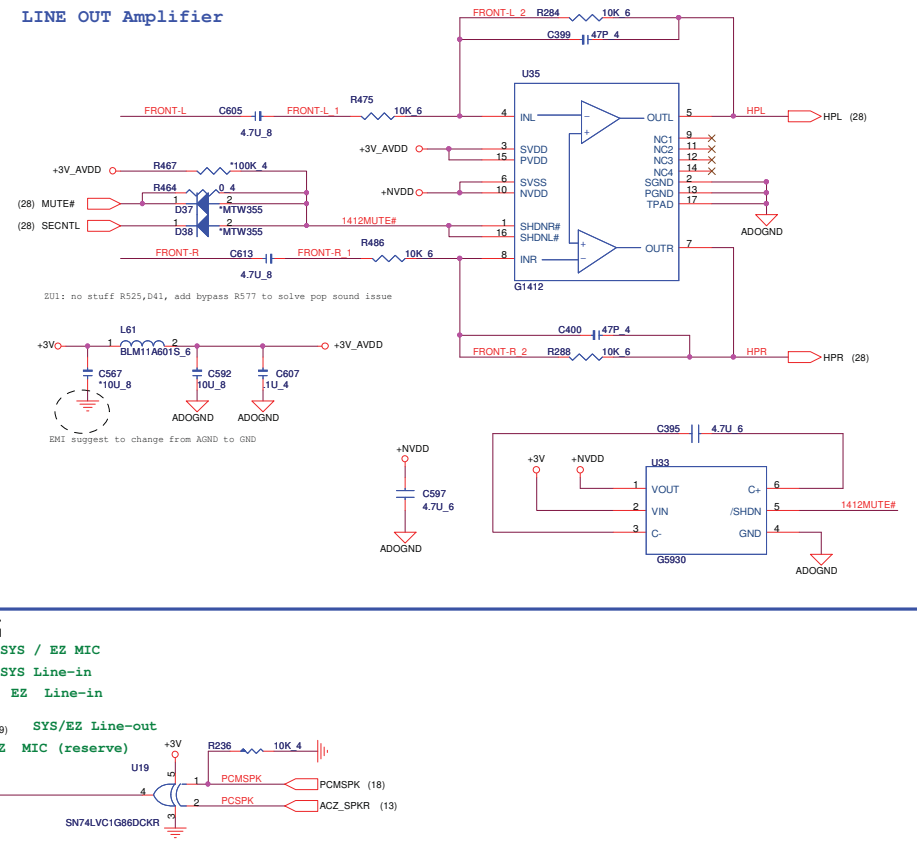




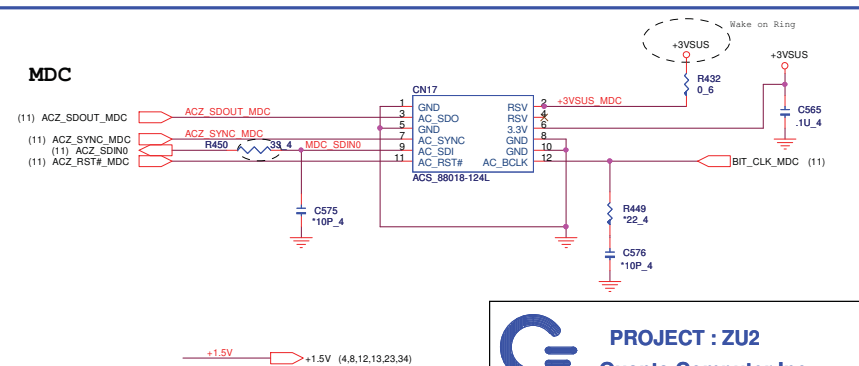
## CODEC (ALC268)



## LINE OUT Amplifier

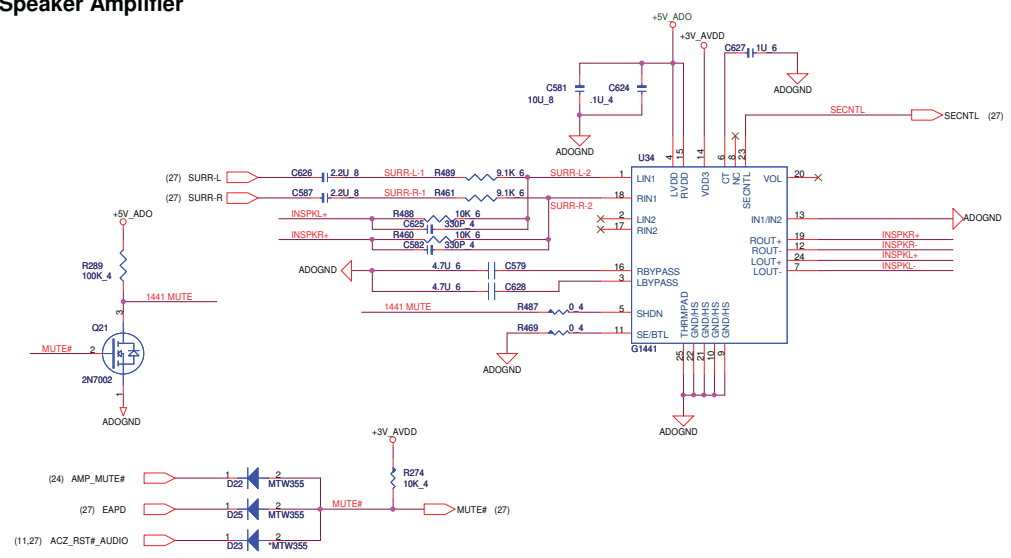


## MDC

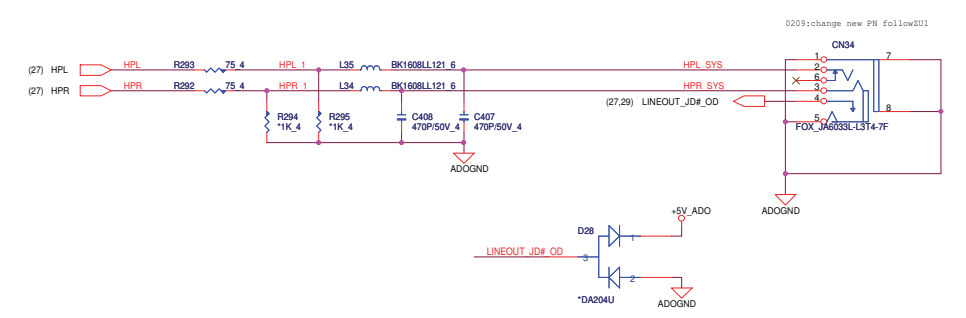




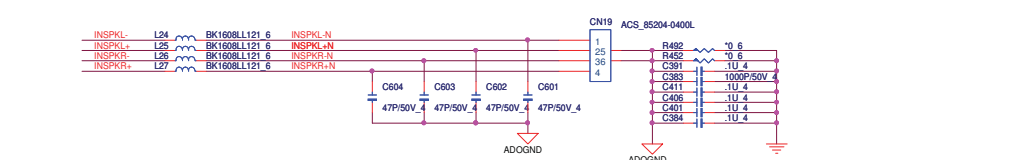
Speaker Amplifier



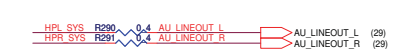
SYSTEM LINE OUT



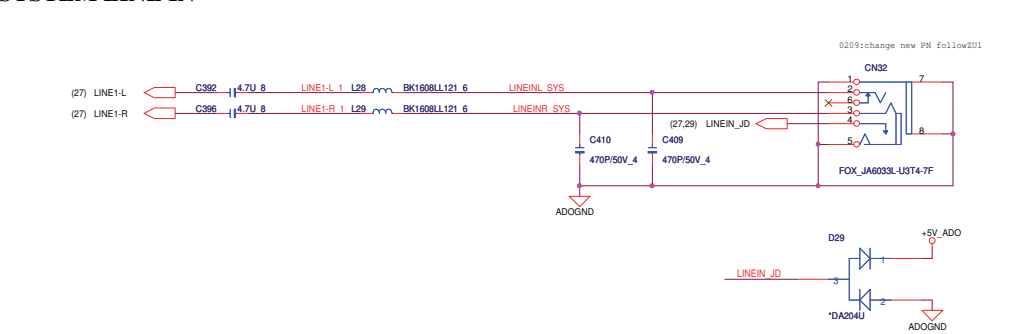
SPEAKER



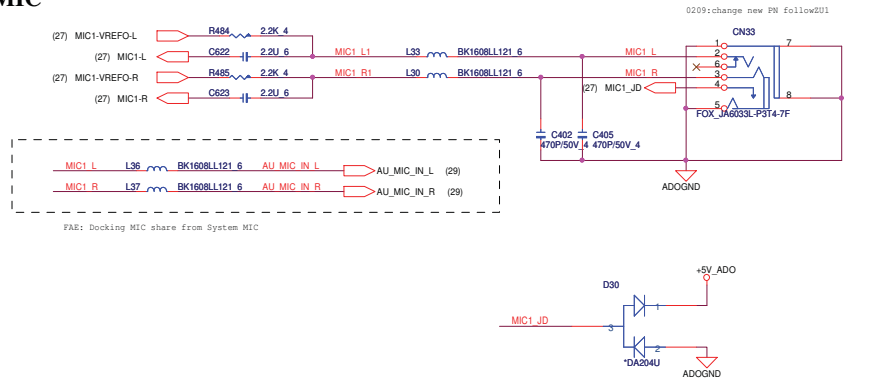
Docking LINE OUT/SPDIF



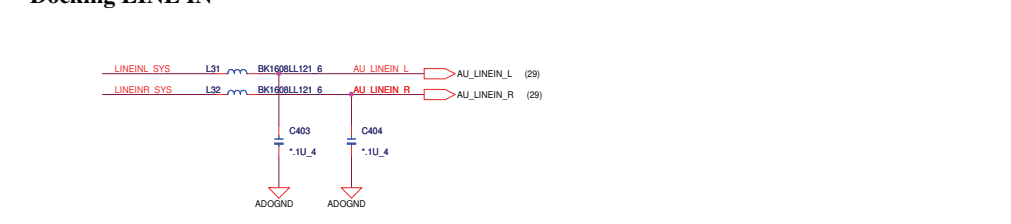
SYSTEM LINE IN



SYSTEM MIC

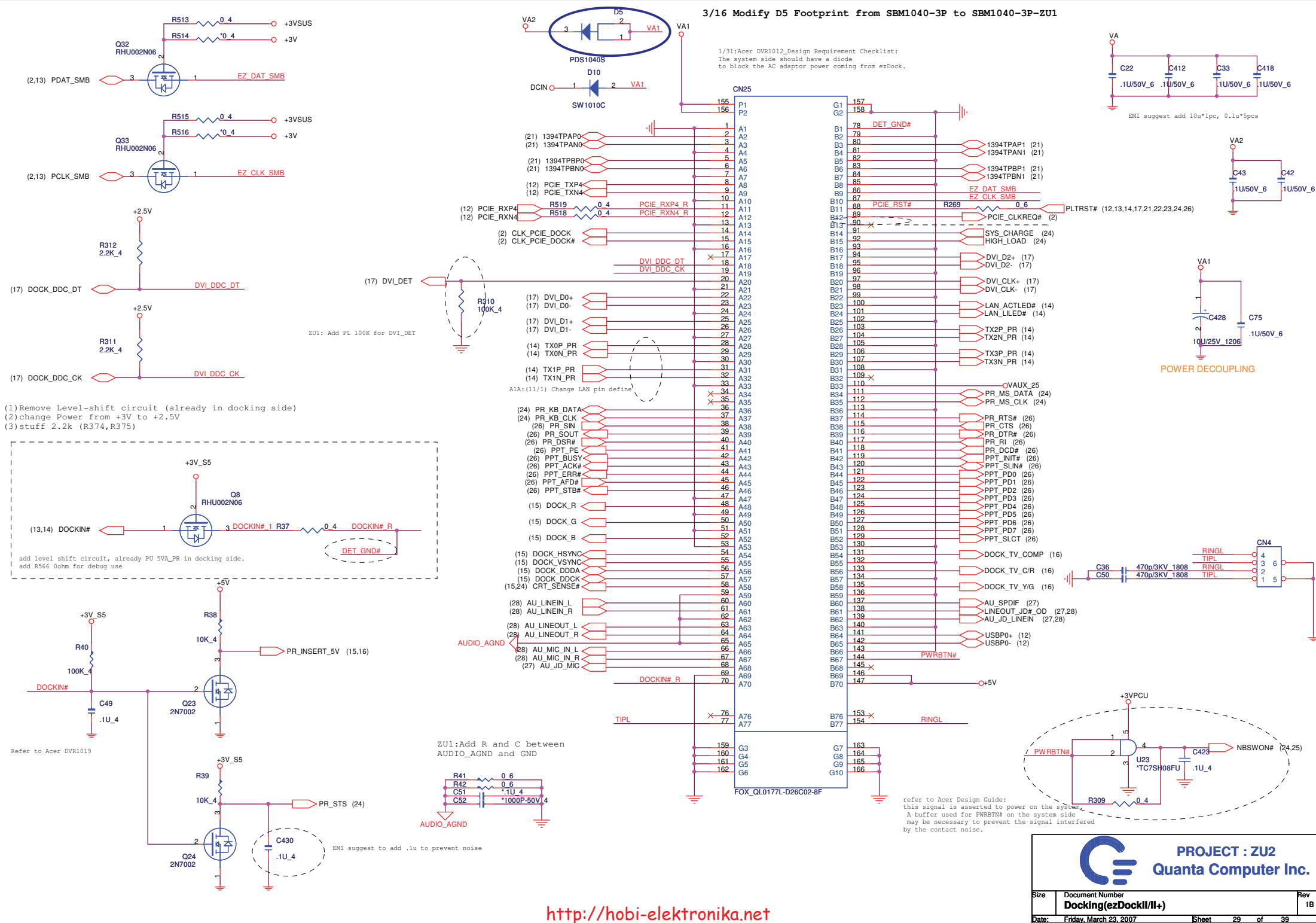


Docking LINE IN



<http://hobi-elektronika.net>





3/16 Modify D5 Footprint from SBM1040-3P to SBM1040-3P-ZU1

1/31:Acer DVR1012\_Design Requirement Checklist:  
The system side should have a diode to block the AC adaptor power coming from ezDock.

EMI suggest add 10u\*1pc, 0.1u\*5pcs

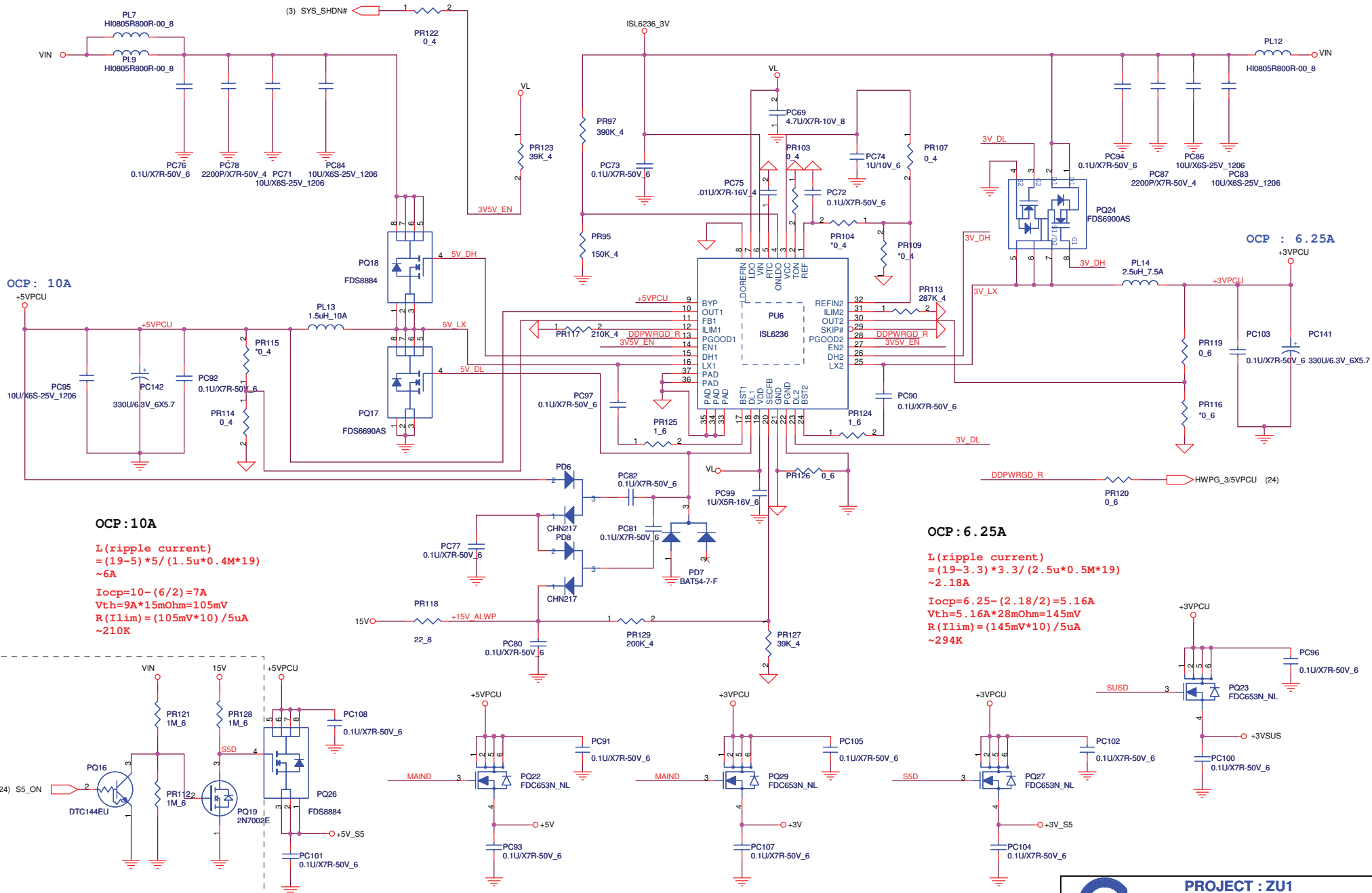
POWER DECOUPLING

refer to Acer Design Guide:  
this signal is asserted to power on the system.  
A buffer used for PWRBTN# on the system side  
may be necessary to prevent the signal interfered  
by the contact noise.

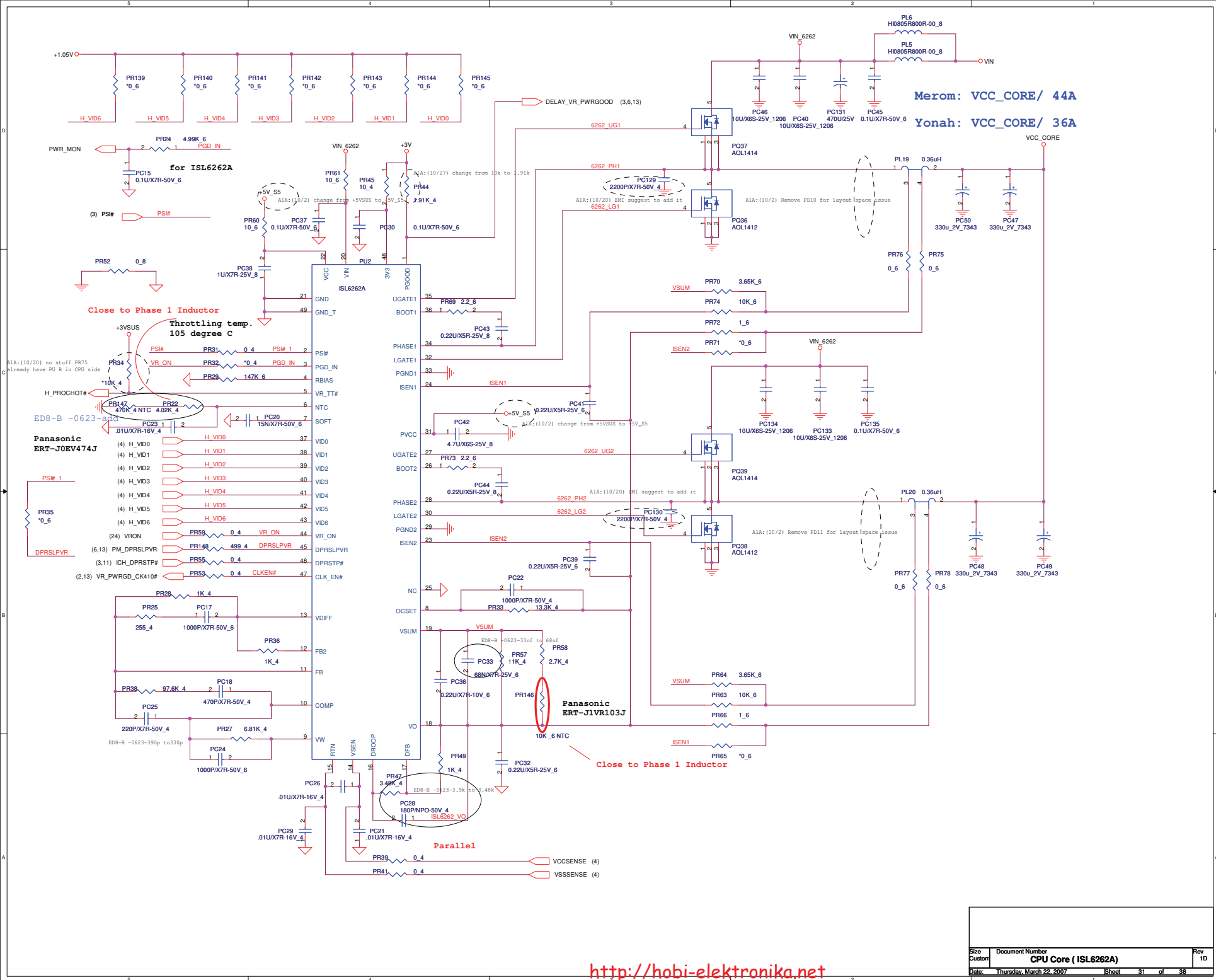


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B1C:(11/30) T211 Power sequence issue  
(1)change PR134 from 0 ohm to 47k ohm.  
(2)stuff C448 0.1uF

A1A:(10/18) Reserve .1uF

A1A:(10/2) change from +5VSUS to +5V\_S5

B1C:(11/29) Change PR8 from 20K(CS32003F933) to 6.65K ohm (CS26653F911)

<http://hobi-elektronika.net>







